

pdp8 /e /f /m Omnibus signals

SWITCH L D2V
KEY CONT L D2U
GND D2T
STOP L D2S
PULSE LA ADDR H D2R
F SET L D2P
GND D2N
USER MODE D2M
E L D2L
D L D2K
F L D2J
IR2 L D2H
GND D2F
IR1 L D2E
IR0 L D2D
GND D2C
-15V D2B
+15V D2A

D1V
D1U
D1T
D1S
D1R
D1P
D1N
D1M
D1L
D1K
D1J
D1H
D1F
D1E
D1D
D1C
D1B
D1A

DATA11 L
DATA10 L
GND
DATA9 L
DATA8 L
MD11 L
GND
MD10 L
MD9 L
MD8 L
MA11 L
MA10 L
GND
MA9 L
MA8 L
GND
N.C.
N.C.

IND2 L C2V
IND1 L C2U
GND C2T
LINK LOAD L C2S
LINK DATA L C2R
TS4 L C2P
GND C2N
TS3 L C2M
TS2 L C2L
TS1 L C2K
TP4 C2J
TP3 C2H
GND C2F
TP2 L C2E
TP1 L C2D
GND C2C
-15V C2B
+5V C2A

C1V
C1U
C1T
C1S
C1R
C1P
C1N
C1M
C1L
C1K
C1J
C1H
C1F
C1E
C1D
C1C
C1B
C1A

MS,I R DISABLE L
CPMA DISABLE L
GND
SKIP L
INITIALIZE
INT RQST L
GND
NOT LAST TRANSF. L
INTER. I/O L
BUS STROBE
C2 L
C1 L
GND
C0 L
I/O PAUSE L
GND
N.C.
N.C.

POWER OK B2V
RUN L B2U
GND B2T
RES2 B2S
RES1 B2R
INT IN PROG H B2P
GND B2N
LOADD ENABLE L B2M
BREAK CYCLE L B2L
BRK DATA CONT L B2K
OVERFLOW L B2J
MA,MS LOAD CONT L B2H
GND B2F
BRK IN PROG L B2E
INT STROBE B2D
GND B2C
-15V B2B
+5V B2A

B1V
B1U
B1T
B1S
B1R
B1P
B1N
B1M
B1L
B1K
B1J
B1H
B1F
B1E
B1D
B1C
B1B
B1A

DATA7 L
DATA6 L
GND
DATA5 L
DATA4 L
MD7 L
GND
MD6 L
MD5 L
MD4 L
MA7 L
MA6 L
GND
MA5 L
MA4 L
GND
N.C.
N.C.

LINK L A2V
ROM ADDR L A2U
GND A2T
WRITE L A2S
RETURN A2R
INHIBIT A2P
GND A2N
STROBE A2M
SOURCE A2L
MD DIR L A2K
MEM START L A2J
EMA2 L A2H
GND A2F
EMA1 L A2E
EMA0 L A2D
GND A2C
-15V A2B
+5V A2A

A1V
A1U
A1T
A1S
A1R
A1P
A1N
A1M
A1L
A1K
A1J
A1H
A1F
A1E
A1D
A1C
A1B
A1A

DATA3 L
DATA2 L
GND
DATA1 L
DATA0 L
MD3 L
GND
MD2 L
MD1 L
MD0 L
MA3 L
MA2 L
GND
MA1 L
MA0 L
SP GND
N.C.
N.C.

Solder Side

Part Side
(facing front of machine)