

PDP-8L

TECHNICAL INFORMATION
FOR INTERNAL USE ONLY

EP-PDP8L-TI-C
COPYRIGHT © 1982
FICHE 1 OF 1

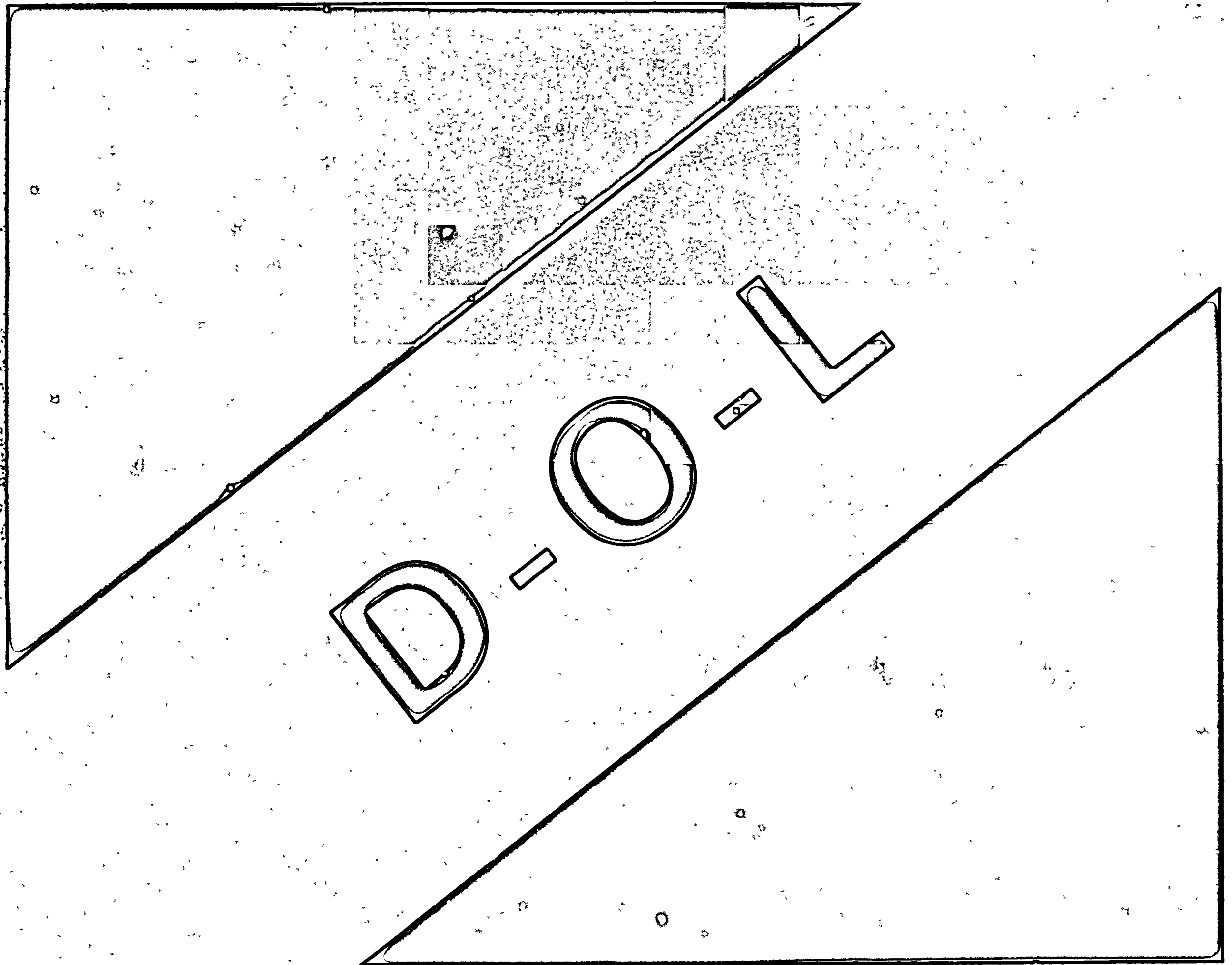
MAR 1982
digital
MADE IN USA

The image displays a grid of technical diagrams and charts for the PDP-8L. The grid is organized into sections:

- PDP8L:** Located in the top-left corner.
- D-O-L:** Located in the second row, first column.
- FCO's:** Located in the third row, second column.
- TECH. TIPS:** Located in the eighth row, tenth column.

The grid contains numerous small diagrams, charts, and tables, likely representing various technical specifications and performance metrics for the PDP-8L. The diagrams include circuit diagrams, timing diagrams, and data tables. The charts show various performance metrics over time or under different conditions. The tables provide numerical data for various parameters.

PDP8L



Engineering Change Order Log

ECO's are updated and published continuously in conjunction with the issuance of Engineering Change Orders for all DEC products and are available upon subscription from:

DIGITAL EQUIPMENT CORPORATION
FIELD SERVICE INFORMATION CENTER
MAYNARD, MASSACHUSETTS 01754

EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ECO SYNOPSIS FOR LOGIC OR OPTION
PDP-8L PROCESSOR LOGIC **PDP-8L**

PRODUCT LINE PDP-8L	PUBLICATION DATE OF THIS SYNOPSIS PAGE JUNE 1978	PAGE REVISION 0
------------------------	---	--------------------

R643

ECO NO.	LOGIC OR OPTION SERIAL NO.'S AFFECTED	FIELD CODE	SYNOPSIS
8L 00001	>8L	M	AUG 68 - CHANGES THE SILK SCREEN FOR THE CONTROL PANEL TO POSITION WORDING CORRECTLY.
8L 00002	>8L	M	AUG 68 - CHANGES THE SILK SCREEN FOR THE CONTROL PANEL TO POSITION WORDING CORRECTLY.
8L 00003	>8L	M	AUG 68 - CHANGES THE SILK SCREEN FOR THE CONTROL PANEL TO POSITION WORDING CORRECTLY.
8L 00004	>8L	M	AUG 68 - CHANGES THE SILK SCREEN ARTWORK FOR THE REAR SCREEN TO IMPROVE MECHANICAL ALIGNMENT.
8L 00005	>8L	M	SEP 68 - SPECIFIES THE USE OF A SMALLER INSERT FOR THE LIGHT BOARD SUPPORT.
8L 00006	N.A.	M	SEP 68 - ADDS A DIMENSION TO THE PRINT FOR THE PLEXIGLAS® SUPPORT

THIS PAGE CONTINUED AND OR ENLARGED BELOW

PDP8L-DOL-1

BL 00007	>BL	M	SEP 68 - ADDS AN INSTRUCTION FOR PREPARATION OF THE SWITCH MOUNTING HOLE IN THE INDICATOR PANEL.
BL 00008	>BL	M	OCT 68 - ADDS RIVNUT CLEARANCE IN THE WIRING BLOCK.
BL 00009	BL 100-169>	D	OCT 68 - MAKES PP8L AND PR8L LOGIC COMPATIBLE WITH THE LATEST REVISION M705 AND M715. CHANGES IOP'S FROM 600 NSEC TO 500 NSEC.
BL 00010	>BL	M	OCT 68 - SPECIFIES TRIMMING OF THE INDICATOR PANEL SWITCH OPENING TO FACILITATE INSTALLATION.
BL 00011	BL 100-199>	P	OCT 68 - PROVIDES A DESIGN UPDATING OF THE BL PRINT SET.

LEGEND

FIELD CODE
P - Field action may be required
D - Design ECO
P - Print or Wire List change
M - Mechanical ECO

SYMBOL
> - ECO applicable to future production

ECO CHANGES
Changes are coded within the system. (*BX,**BY,**BZ)
BX - Change for space and updated prints only
BY - Change for necessary parts only
BZ - Change for on site labor only. Installation by DDC

NOTE: Changes are added (BX+BY+BZ = XXX) on the change for ECO installation by DDC

MASTER DRAWING LIST REVISIONS	
REV	ECO NUMBER
A	BL-00009
B	BL-00011

WIRE LIST REVISIONS	
REV	ECO NUMBER
A	BL-00009
B	BL-00011

BL PAGE 1

ONLY FCO'S WRITTEN TO THE "PDP8L" OPTION ARE DOCUMENTED ON THIS FICHE.

FCO'S FOR BA08 ARE DOCUMENTED IN ITS APPROPRIATE PLACE IN THIS SECTION OF THE LIBRARY.

FCO'S FOR "G020", G021, G221, G624, & G793" ARE DOCUMENTED IN THE MODULE ASSEMBLY (BLUE) SECTION OF THE LIBRARY.

THIS PAGE CONTINUED FROM FRAME ABOVE

PDP8L-DOL-2

BL NO.	LE	FIELD CODE	SYNOPSIS
BL 00012	>BL	M	OCT 68 - CHANGES THE SUPER CHASSIS COVER MATERIAL FROM ENBOSSED STEEL TO PLAIN STEEL.
BL 00013	BL 100-199>	F	OCT 68 - CORRECTS AN IOP 4 RACE CONDITION CAUSED BY "I/O ON (1)" AND "IOP C (0)" BEING GENERATED SIMULTANEOUSLY BY I/O ROTATE.
G221 00001	ALL BL	F	OCT 68 - SPECIFIES THAT ALL DEC 6534B TRANSISTORS ARE TO BE CHANGED TO DEC 2904 TO PROVIDE MORE DEPENDABLE OPERATION UNDER HIGH VOLTAGE CONDITIONS. G221 CIRCUIT SCHEMATIC REVISION C
BL 00014	N.A.	P	OCT 68 - CORRECTS MODULE BLOCK DESIGNATIONS ON THE WIRED ASSEMBLY DRAWING.
BL 00015	>BL	M	OCT 68 - INCREASES INSERT HOLE SIZE TO ELIMINATE SPLITTING OF THE CORNERS OF THE BENELEX®.
BL 00016	>BL	M	OCT 68 - CHANGES THE LIGHT PANEL EXTRUSION TO HOLD THE SWITCHES MORE FIRMLY.
BL 00017	BL 100-199>	P	OCT 68 - CORRECTS WIRE LIST ERRORS.
BL 00018	>BL	M	OCT 68 - CHANGES THE POSITION OF A HOLE IN THE CHASSIS TO PROVIDE CORRECT ALIGNMENT WITH AN INSERT IN THE POWER SUPPLY.

THIS PAGE
CONTINUED AND OR
ENLARGED BELOW

PDP8L-DOL-3

G624 00001	ALL 8L FIELD RETROFIT AS REQUIRED	F	NOV 68 - CHANGES THE VALUE OF THE INHIBIT CURRENT LIMITING RESISTORS FROM 70 OHMS TO 56 OHMS. THIS INCREASES THE INHIBIT CURRENT AND IMPROVES THE OPERATION OF FERROX CUBE STACKS. G624 CIRCUIT SCHEMATIC REVISION D
8L 00019	>8L	M	NOV 68 - INCREASES CLEARANCE FOR THE SWITCHES ON THE INDICATOR PANEL.
8L 00020	8L 100-227>	P	NOV 68 - CORRECTS WIRE LIST ERRORS.
8L 00021	>8L	M	NOV 68 - CHANGES THE COLOR SCHEME FOR THE INDICATOR PANEL.
8L 00022	8L 101-325>	F	NOV 68 - CHANGES TIMING ON "KEY CL" AND "KEY LOAD" TO ALLOW SETTING OF THE EA LIGHT WITH LOAD ADDRESS WHEN THE SYSTEM INCLUDES EXTENDED MEMORY. ADDS A GATE TO ELIMINATE THE SR BEING LOADED INTO BOTH THE MA AND THE MB. PROVIDES "BEMA" TO THE MCBL INSTEAD OF "EMA"; THIS EXTENDS MEMORY PROTECTION TO INCLUDE THE LAST PAGE OF EXTENDED MEMORY. GATES "WORD COUNT 0" TO PROVIDE MEMORY PROTECT DURING A THREE-CYCLE BREAK.

LEGEND

FIELD CODE
 F - Field action may be required
 D - Design BCO
 P - Print or Wire List change
 M - Mechanical BCO

SYMBOL
 > - BCO applicable to future production

BCO CHANGES
 Changes are coded within the synopsis. (*SX,**SY,**SZ)
 SX - Change for Space and updated prints only
 SY - Change for necessary parts only
 SZ - Change for on site labor only. Installation by DSC
 NOTE: Changes are additive (SX+SY+SZ = Total on site change for BCO installation by DSC)

MASTER DRAWING LIST REVISIONS	
REV	BCO NUMBER
C	8L-00013
D	8L-00017
E	8L-00020
F	8L-00022

WIRE LIST REVISIONS	
REV	BCO NUMBER
C	8L-00013
D	8L-00017
E	8L-00020
F	8L-00022

THIS PAGE CONTINUED FROM FRAME ABOVE

PDP8-DOL-4

Engineering Change Order Log

These are updated and published continuously in conjunction with the
 ENGINEERING CHANGE ORDERS for all DEC products and are available
 upon subscription from:

DIGITAL EQUIPMENT CORPORATION
 FIELD SERVICE INFORMATION CENTER
 MAYNARD, MASSACHUSETTS 01754

**EQUIPMENT
 CORPORATION**
 MAYNARD, MASSACHUSETTS

BCO SYNOPSIS FOR LOGIC OR OPTION

PDP-8L PROCESSOR LOGIC **PDP-8/L**

PRODUCT LINE	PUBLICATION DATE OF THIS SYNOPSIS PAGE	PAGE REVISION
PDP-8L	JUNE 1978	8

R643

BCO NO.	LOGIC OR OPTION SERIAL NO.'S AFFECTED	FIELD CODE	SYNOPSIS
BL 00023	N.A.	P	NOV 68 - MAKES AN INCIDENTAL CORRECTION TO AN 8L BLOCK SCHEMATIC PRINT.
BL 00024	>8L	M	DEC 68 - CHANGES INSERTS IN THE CHASSIS FRAME TO PROVIDE GREATER THREAD DEPTH.
BL 00025	N.A.	P	JAN 69 - CORRECTS PRINTS.
BL 00026	N.A.	P	JAN 69 - CORRECTS MODULE QUANTITIES ON THE PARTS LIST. ADDS SPECIFICATIONS AND ACCEPTANCE PROCEDURES TO THE 8L PRINT SET.
BL 00027	8L 100-200>	F	JAN 69 - SPECIFIES THE USE OF TWISTED PAIR SENSE AMP LINES TO ELIMINATE NOISE PICKUP. *\$5.00
BL 00028	AS ORDERED	M	JAN 69 - ADDS ART WORK TO PROVIDE A SPECIAL CONTROL PANEL VARIATION AT CUSTOMER REQUEST.
BL 00029	N.A.	M	FEB 69 - CHANGES THE DIMENSIONS OF AN ALUMINUM STRIP ON THE SHIPPING CRATE.

THIS PAGE
 CONTINUED AND OR
 ENLARGED BELOW

PDP8L-DOL-5

BL 00030	BL 1-692> ALL WITH BA08	F	FEB 69 - ADDS A SIGNAL TO ELIMINATE FLOATING GATES IN THE BA08 DURING BL POWER SHUTDOWN. NOTE: THE NET EFFECT OF ECO'S BL-00030, 00031 AND 00032 IS TWO "ADDS", A2BE1 TO B36T2 AND B27F2 TO B36S2. REFERENCE ECO BA08-00005. *35.00 (ERROR CORRECTED BY ECO 81-00031)
BL 00031	BL 1-692> ALL WITH BA08	F	FEB 69 - CORRECTS ECO BL-00030; DELETES A36S2 TO B36S2. NOTE: THE NET EFFECT OF ECO'S BL-00030, 00031 AND 00032 IS TWO ADDS, A2BE1 TO B36T2 AND B27F2 TO B36S2. REFERENCE ECO BA08-00005. INCLUDED IN THE BL-00030 KIT
BL 00032	>BL	M	FEB 69 - ADDS HOLE PUNCHING AND INSERT INSTALLATION INSTRUCTIONS TO THE SUPER COVER DRAWING.

<p align="center">LEGEND</p> <p>FIELD CODE</p> <p>F - Field action may be required D - Design ECO P - Print or Wire List change M - Mechanical ECO</p> <p>SYMBOL</p> <p>> - ECO applicable to future production</p> <p>ECO CHANGES</p> <p>Changes are coded within the synopsis. (*SX,**SY,***SZ) SX - Change for Space and updated price <u>only</u> SY - Change for necessary parts <u>only</u> SZ - Change for or site labor <u>only</u>. Installation by DDC</p> <p>NOTE: Changes are additive (SX+SY+SZ = Total) on the change for ECO installation by DDC</p>	MASTER DRAWING LIST REVISIONS				WIRE LIST REVISIONS			
	REV	ECO NUMBER	REV	ECO NUMBER	REV	ECO NUMBER	REV	ECO NUMBER
	M	BL-00023			M	BL-00027		
	J	BL-00025			J	BL-00030		
	K	BL-00026						
	L	BL-00027						
	M	BL-00028						
	N	BL-00030						

BL PAGE 3

THIS PAGE
CONTINUED FROM
FRAME ABOVE

PDP8L-DOL-6

ECO NO.	ECO'S AFFECTED	FIELD CODE	SYNOPSIS
BL 00033	>BL	M	FEB 69 - ADDS HOLE PUNCHING AND INSERT INSTALLATION INSTRUCTIONS TO THE CABLE RETAINER DRAWING.
BL 00034	>BL	M	FEB 69 - ADDS DIMENSIONS, HOLE PUNCHING AND INSERT INSTALLATION INSTRUCTIONS TO THE CHASSIS FRAME DRAWINGS.
BL 00035	>BL	M	FEB 69 - CHANGES DIMENSIONS ON THE CHASSIS SUPER COVER DRAWING.
BL 00036	>BL	M	FEB 69 - CORRECTS THE PARTS LIST TO INCLUDE A CORRECT PART NUMBER FOR FOAM BACK. 46.
BL 00037	>BL	M	FEB 69 - ADDS A WELDED CORNER BRACE TO PREVENT CHASSIS MOVEMENT.
BL 00038	BL 693> ALL WITH BAOB	F	FEB 69 - CHANGES LOGIC, POWER SUPPLY AND 0826 REGULATOR CONTROL TO ELIMINATE POWER ON-OFF PROBLEMS. NOTE: THE CHANGE NUMBER ON THE SPECO FACE SHEET IS 00036 IN ERROR. NOTE: THE NET EFFECT OF ECO'S BL-00030, 00031 AND 00038 IS TWO "ADDS" A28E1 TO B36T2 AND B27F2 TO B3652. *85.00
BL 00039	>BL	P	MAR 69 - CHANGES THE DRAWING FOR BRACKET #2 TO INCLUDE A PEM STUD RATHER THAN A WELDED STUD.
BL 00040	>BL	P	MAR 69 - CHANGES THE DRAWING FOR BRACKET #1 TO INDICATE PUNCHING AND INSERT INSTALLATION DIRECTIONS.

THIS PAGE
CONTINUED AND OR
ENLARGED BELOW

PDP8L-DOL-7

BL 00041	>BL	M	MAR 69 - ADDS INSTRUCTIONS FOR THE BONDING OF THE DECORATIVE CASING TO THE CHASSIS.
BL 00042	>BL	'	MAR 69 - REVISES BEZEL DRAWING DIMENSIONS TO AGREE WITH THE MANUFACTURED BEZEL.
BL 00043	>BL	M	MAR 69 - ADDS A METAL BORDER TO THE HEX SCREEN FOR A STRONGER WELD.
BL 00044	BL 100-720> ALL WITH MPBL	F	MAR 69 - CHANGES "MP SKIP" GATE INPUT FROM THE "1" SIDE OF THE PARITY ERROR FLIP-FLOP TO THE "0" SIDE. *\$5.00
BL 00045	BL 1000>	F	APR 69 - CHANGES LOGIC TO ALLOW FAST FLAG TESTING IN PERIPHERALS. THIS IS A PRODUCT IMPROVEMENT MODIFICATION. (ERRORS CORRECTED BY ECO'S BL-00056, 00059 AND 00062) *\$5.00

LEGEND	
FIELD CODE	
F	Field action may be required
D	Design BCO
P	Print or Wire List change
M	Mechanical BCO
SYMBOL	
>	BCO applicable to future production
BCO CHANGES	
Changes are coded within the system. (*GX,*GY,**GZ)	
GX	Change for design and updated parts only
GY	Change for necessary parts only
GZ	Change for on site labor only, installation by BSC
NOTE: Changes are coded for GX-GY-GZ - Type on the change for BCO installation by BSC	

MASTER DRAWING LIST REVISIONS					
REV	ECO NUM	DER	REV	ECO NUM	ER
P	BL-00036				
R	BL-00038				
S	BL-00044				
T	BL-00045				

WIRE LIST REVISIONS					
REV	ECO NUM	R	REV	ECO NUM	R
K	BL-00038				
L	BL-00044				
M	BL-00045				

BL PAGE 4

THIS PAGE
CONTINUED FROM
FRAME ABOVE

PDP8L-DOL-8

Engineering Change Order Log

ECO's are updated and published continuously in conjunction with the issuance of ENGINEERING CHANGE ORDERS for all DEC products and are available upon subscription from:

DIGITAL EQUIPMENT CORPORATION
 FIELD SERVICE INFORMATION CENTER
 MAYNARD, MASSACHUSETTS 01754

EQUIPMENT CORPORATION
 MAYNARD, MASSACHUSETTS

ECO SYNOPSIS FOR LOGIC ON OPTION
 PDP-8L PROCESSOR LOGIC **PDP-8/L**

PRODUCT LINE PDP-8L	PUBLICATION DATE OF THIS SYNOPSIS PAGE JUNE 1978	PAGE REVISION 8
------------------------	---	--------------------

0170 R628

ECO NO.	LOGIC OR OPTION SERIAL NO.'S AFFECTED	FIELD CODE	SYNOPSIS
8L 88846	AS ORDERED	M	APR 69 - PROVIDES THE SPECIAL ARTWORK REQUIRED FOR A CUSTOMER REQUESTED VARIATION TO THE CONTROL PANEL.
8L 88847	>8L	M	APR 69 - STANDARDIZES CHASSIS SLIDE SPECIFICATIONS.
8L 88848	N.A.	P	MAY 69 - ADDS AN IN-PLANT TEST PROCEDURE TO THE 8L PRINT SET.
8L 88849	>8L	M	MAY 69 - ADDS A SHIPPING LOCK FEATURE TO THE CHASSIS SLIDES.
8L 88850	>8L	M	MAY 69 - REDUCES THE HEIGHT OF THE 1-3/4" FILLER PANEL.
8L 88851	N.A.	P	MAY 69 - CORRECTS A DRAFTING ERROR ON A PP8L PRINT.
8L 88852	>8L	M	MAY 69 - REPLACES AN ALUMINUM ANGLE ON THE SUPER BOX WITH A PLASTIC STRIP TO PREVENT THE MARRING UP TABLE SURFACES.

THIS PAGE CONTINUED AND OR ENLARGED BELOW

PDP8L-DOL-9

8L 00053	N.A.	P	JUN 69 - ADDS DRAWINGS, ENGINEERING SPECIFICATIONS AND ACCEPTANCE PROCEDURE TO THE PC8L PRINT SET.
8L 00054	N.A.	P	JUN 69 - ADDS SEVERAL DRAWINGS TO THE PP8L PRINT SET.
8L 00055	N.A.	P	JUN 69 - ADDS PRINTS, ENGINEERING SPECIFICATIONS, AND ACCEPTANCE PROCEDURE TO THE PR8L PRINT SET.
8L 00056	8L 1000-1421 > ALL WITH ECO 8L-00045	F	JUN 69 - CORRECTS ERRORS IN ECO 8L-00045; ADDS AN M115 AND CORRECTS LOGIC TO ALLOW PROPER INTERRUPT OPERATION. INCLUDED IN THE 8L-00045 KIT) (ERROR CORRECTED BY ECO 8L-00092)
8L 00057	8L 0761 ONLY	D	JUN 69 - ADDS CUSTOMER-REQUESTED JMS/0000 SWITCH LOGIC AND "PAUSE" TIMING MODIFICATION.

LEGEND

FIELD CODE
 F - Field action may be required
 D - Design ECO
 P - Print or Wire List change
 M - Mechanical ECO

SYMBOL
 > - ECO applicable to future production

ECO CHARGES
 Changes are coded within the synopsis. (*SX,**SY,**SZ)
 SX - Charge for Space and updated prints only
 SY - Charge for necessary parts only
 SZ - Charge for on site labor only, installation by DEC

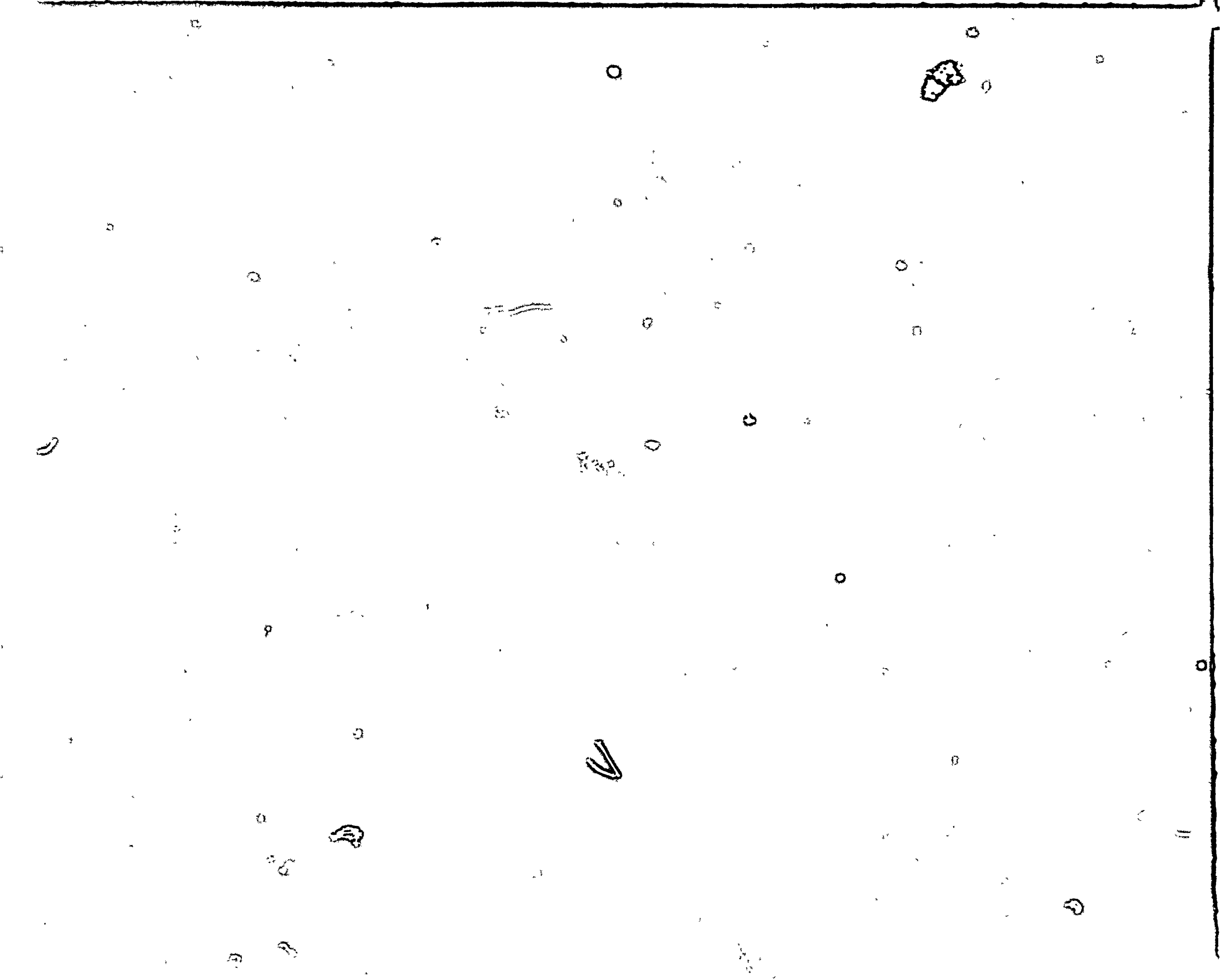
NOTE Changes are additive (SX+SY+SZ - [] on site charge for ECO installation by DEC)

MASTER DRAWING LIST REVISIONS			
REV	ECO NUMBER	REV	ECO NUMBER
U	8L-00046		
V	8L-00047		
W	8L-00048		
Y	8L-00056		

WIRE LIST REVISIONS			
REV	ECO NUMBER	REV	ECO NUMBER
N	8L-00056		

THIS PAGE
CONTINUED FROM
FRAME ABOVE

PDP8L-DOL-10



ECO NO.	LOGIC OR OPTION SERIAL NO.'S AFFECTED	FIELD CODE	SYNOPSIS
BL 00058	>BL	M	JUN 69 - CHANGES PLEXIGLAS [®] SUPPORT HOLE SPECIFICATION.
BL 00059	BL 1000-1471 > ALL WITH ECO BL-00045	F	JUN 69 - CORRECTS AN ERROR IN ECO BL-00045; ADDS "POWER CLEAR" TO THE PAUSE FLIP-FLOP TO ELIMINATE THE REQUIRED DOUBLE TOGGING OF THE LOAD ADDRESS SWITCH WHEN PAUSE COMES ON IN THE "1" STATE AT POWER UP AND DISABLES THE PC. ERROR CORRECTED BY ECO BL-00062
BL 00060	>BL	M	JUN 69 - INCREASES A WELD AREA FOR BETTER SUPPORT OF THE CHASSIS FRAME.
BL 00061	>BL	M	JUN 69 - MODIFIES THE CHASSIS SLIDES.
BL 00062	BL 1000-1511 > AND ALL WITH ECO BL-00059	F	JUL 69 - CORRECTS ECO BL-00059; ADDS A MISSING WIRE AND REVERSES THE B13P1 AND 713R1 INPUTS TO THE M113.
BL 00063	N.A.	P	JUL 69 - CORRECTS PIN NUMBER DESIGNATIONS FOR A LOGIC GATE.
BL 00064	>BL	M	JUL 69 - CHANGES THE DIRECTION OF INSERT INSTALLATION ON A 718 POWER SUPPLY BRACKET.

THIS PAGE
CONTINUED AND OR
ENLARGED BELOW

PDP8L-DOL-11

BL 00065	>BL	M	JUL 69 - DELETES 90-DEGREE CORNERS ON THE PLEXIGLAS® SUPPORT.
BL 00066	N.A.	P	AUG 69 - ADDS ACCESSORY AND SOFTWARE LISTS TO THE BL PRINT SET.
BL 00067	N.A.	P	AUG 69 - ADDS ENGINEERING SPECIFICATION TEST PROCEDURE TO THE MP8L PRINT SET.
BL 00068	BL 1-1429>	F	AUG 69 - CHANGES THE POLARITY OF "INITIALIZE" ON THE BL BUS TO BE 8I COMPATIBLE.
BL 00069	N.A.	M	AUG 69 - CHANGES DRILLING INSTRUCTIONS.
BL 00070	N.A.	M	SEP 69 - ADDS CHAD BOX GLUEING INSTRUCTIONS.

LEGEND

FIELD CODE
 F - Field action may be required
 D - Design ECO
 P - Print or Wire List change
 M - Mechanical ECO

SYMBOL
 > - ECO applicable to future production

ECO CHANGES
 Changes are coded within the system. (*SX,**SY,***SZ)
 SX - Change for Space and updated prints only
 SY - Change for necessary parts only
 SZ - Change for on site labor only, installation by DEC
 NOTE: Changes are additive (SX+SY+SZ = Total on site change for ECO installation by DEC)

MASTER DRAWING LIST REVISIONS			
REV	ECO NUMBER	REV	ECO NUMBER
Z	BL-00059		
AA	BL-00062		
AB	M960B-00001		
AC	BL-00063		
AD	BL-00066		
AE	BL-00068		

WIRE LIST REVISIONS			
REV	ECO NUMBER	REV	ECO NUMBER
P	BL-00059		
R	BL-00062		
S	BL-00068		

THIS PAGE
CONTINUED FROM
FRAME ABOVE

PDP8L-DOL-12

Engineering Change Order Log

ECO's are prepared and published continuously in conjunction with the issuance of Engineering Change Orders for all DEC products and are available upon subscription from:

DIGITAL EQUIPMENT CORPORATION
FIELD SERVICE INFORMATION CENTER
MAYNARD, MASSACHUSETTS 01754

EQUIPMENT CORPORATION

MAYNARD, MASSACHUSETTS

ECO SYNOPSIS FOR LOGIC OR OPTION

PDP-8L PROCESSOR LOGIC

PDP-8L

PRODUCT LINE

PUBLICATION DATE OF THIS SYNOPSIS PAGE

PAGE REVISION

PDP-8L

JULY 1970

0

199 R5349

ECO NO.	LOGIC OR OPTION SERIAL NO.'S AFFECTED	FIELD CODE	SYNOPSIS
BL 00071	BL 1-1911> 6826 REV F>	F	SEP 69 - SPECIFIES THE REMOVAL OF THE BUS BETWEEN B27B2 AND B28B2 TO ELIMINATE BOTH -15V SUPPLIES BEING SHORTEED TOGETHER BY A JUMPER ON THE 6826 MODULE. *\$5.00
BL 00072	N.A.	M	SEP 69 - CHANGES THE FAN PART NUMBER AND ITS MOUNTING.
BL 00073	BL WITH 761 LOGIC ONLY	D	SEP 69 - MAKES CUSTOMER-REQUESTED CHANGES TO BL SYSTEM #754 ONLY. ALLOWS A SWITCH TO FORCE A JMS TO 0000 AND HOLDS THE PAUSE FLIP-FLUP ON FOR 1.5 USEC DURING AN IOT CYCLE.
BL 00074	BL 100-1912>	F	SEP 69 - SPECIFIES THE INSTALLATION OF ADDITIONAL GROUND WIRES TO ELIMINATE RANDOM MACHINE PROBLEMS. *\$5.00
BL 00075	>BL	M	OCT 69 - ELONGATES A HOLE IN THE FRAME TO FACILITATE MOUNTING OF THE 718 POWER SUPPLY WITHIN THE CHASSIS.

THIS PAGE
CONTINUED AND OR
ENLARGED BELOW

PDP8L-DOL-13

BL 00076	>BL	M	OCT 69 - CORRECTS THE MOUNTING BAR ASSEMBLY DRAWING TO INDICATE THE REQUIRED QUANTITIES OF MECHANICAL HARDWARE.
BL 00077	N.A.	M	OCT 69 - CHANGES A HOLE DIMENSION IN THE SHIPPING CRATE.
BL 00078	BL 1800-2054> FIELD METHOD IT AS REQUIRED	F	OCT 69 - ADDS KEY LOGIC TO CLEAR THE INTERRUPT SYNC FLIP-FLOP TO RESOLVE AN INABILITY TO DEPOSIT OR EXAMINE IN UPPER MEMORY WHEN AN EXTERNAL DEVICE HAS GENERATED AN INTERRUPT FOLLOWING A CP HALT WITH THE INTERRUPT ON. AN M115 MODULE WAS ADDED IN SLOT C28 BY ECO BL-00056; THE INSTALLATION OF THAT MODULE IS NECESSARY FOR PROPER MACHINE OPERATION FOLLOWING IMPLEMENTATION OF THIS ECO. *\$5.00 (ERROR CORRECTED BY ECO BL-00083)

LEGEND

FIELD CODE

- F - Field action may be required
- D - Design BOC
- P - Print or Wire List change
- M - Mechanical BOC

SYMBOL

- > - BOC applicable to future production

BOC CHANGES

Changes are noted within the synopsis. (*BX,**BY,**BZ)

- BX - Change for Space and updated price only
- BY - Change for necessary parts only
- BZ - Change for on site labor only, installation by BOC

NOTE: Changes are additive (BX+BY+BZ = J) on site change for BOC installation by BOC

MASTER DRAWING LIST REVISIONS			
REV	ECO NUMBER	REV	ECO NUMBER
AF	BL-00071		
AM	MISC-00031		
AJ	BL-00072		
AK	BL-00074		
AL	BL-00076		
AN	BL-00078		

WIRE LIST REVISIONS			
REV	ECO NUMBER	REV	ECO NUMBER
T	BL-00078		

PDP-8L PAGE 7

THIS PAGE
CONTINUED FROM
FRAME ABOVE

PDP8L-DOL-1A

	OR OPTION IS AFFECTED	FIELD CODE	SYNOPSIS
BL 00079	>BL	M	OCT 69 - ADDS A MECHANICAL PERPENDICULARITY SPECIFICATION FOR THE BEZEL.
G020 00003	ALL G020 AFTER 11-1-69	F	SEP 69 - CHANGES THE VOLTAGE DIVIDER RESISTORS ON REVISION "E" ETCHED BOARDS TO INCREASE THE SLICE VOLTAGE FROM -3 VDC TO -4 VDC. THIS PROVIDES INCREASED STROBE MARGINS AND NOISE IMMUNITY. ERROR CORRECTED BY ECO 0020-00006. *\$5.00 G020 CIRCUIT SCHEMATIC REVISION E
G021 00003	ALL G021	F	SEP 69 - CHANGES THE VOLTAGE DIVIDER RESISTORS ON ETCHED BOARDS EARLIER THAN "E" REVISION TO INCREASE THE SLICE VOLTAGE FROM -3 VDC TO -4 VDC. THIS PROVIDES INCREASED STROBE MARGINS AND NOISE IMMUNITY. ERROR CORRECTED BY ECO 0021-00007. *\$5.00 G021 CIRCUIT SCHEMATIC REVISION E
BL 00050	>BL	M	NOV 69 - ENLARGES HOLES TO ALLOW SWITCHES TO OPERATE MORE FREELY.
BL 00051	>BL	M	NOV 69 - REMOVES CAPTIVE SCREWS FROM THE CHASSIS FRAME AND ADDS LOCK WASHERS INSTEAD.
BAB8 00027	BAB8 1-589 ALL BL	F	NOV 69 - RE-ROUTES THE "PWR ON" RUN AND SPECIFIES REVISED G785 AND G826 MODULES TO RESOLVE A POWER FAIL PROBLEM WITH EXTRA MEMORY. BOTH BAB8 AND BL MUST HAVE G785 REVISION "E" OR LATER AND G826 REVISION "H" OR LATER.
BL 00052	BL SYSTEM #754 BL LOGIC #761	D	NOV 69 - MODIFIES IOP LOGIC AT CUSTOMER REQUEST.

THIS PAGE
CONTINUED AND OR
ENLARGED BELOW

PDP8L-DOL-15

BL 00083	BL 1800-2054> ALL BL WITH ECO BL-00078	F	NOV 69 - CORRECTS AN ERROR IN ECO BL-00078; GATES "RUN (0)" WITH KEY FUNCTIONS SO THAT "KEY LA+EX+DP" IS DISABLED WHEN THE BL IS RUNNING.
BL 00084	N.A.	P	NOV 69 - CORRECTS THE WIRE LIST TO SHOW CURRENT LOOPS AND TWISTED PAIRS AS HAND WRAPS.
BL 00085	BL 100-2346>	F	DEC 69 - CORRECTS LOGIC AND SPECIFIES REVISIONS OF 0826 AND 0785 MODULES WHICH MUST BE USED TO PERMIT THE KP8L POWER FAIL OPTION TO OPERATE PROPERLY WITH MC8L. 0785 REVISION E OR LATER AND 0826 REVISION H ARE REQUIRED IN THE BL. 0785 REVISION E OR LATER IS REQUIRED IN THE BABB IF IT IS PRESENT.

LEGEND	
FIELD CODE	
F - Field action may be required	
D - Design ECO	
P - Print or Wire List change	
M - Mechanical ECO	
SYMBOL	
> - ECO applicable to future production	
ECO CHARGES	
Charges are coded within the synopsis. (*BX,**BY,**BZ)	
BX - Charge for space and updated prints <u>only</u>	
BY - Charge for necessary parts <u>only</u>	
BZ - Charge for on site labor <u>only</u> , installation by DEC	
NOTE: Charges are additive (BX+BY+BZ = Total) on site charge for ECO installation by DEC	

MASTER DRAWING LIST REVISIONS			
REV	ECO NUMBER	REV	ECO NUMBER
AN	BL-00083		
AP	BL-00084		
AR	BL-00081		
AS	BL-00085		

WIRE LIST REVISIONS			
REV	ECO NUMBER	REV	ECO NUMBER
U	BL-00083		
V	BL-00084		
W	BL-00085		

PDP-8L PAGE 8

THIS PAGE
CONTINUED FROM
FRAME ABOVE

PDP8L DDL-10

Engineering Change Order Log

is updated and published continuously in conjunction with the
 CHANGE ORDERS for all DEC products and are available
 upon subscription request.

DIGITAL EQUIPMENT CORPORATION
 FIELD SERVICE INFORMATION CENTER
 MAYNARD, MASSACHUSETTS 01754



**EQUIPMENT
 CORPORATION**
 MAYNARD, MASSACHUSETTS

ECO SYNOPSIS FOR LOGIC OR OPTION

PDP-8L PROCESSOR LOGIC **PDP-8/L**

PRODUCT LINE	PUBLICATION DATE OF THIS SYNOPSIS PAGE	PAGE REVISION
PDP-8L	JULY 1978	0

R6082

ECO NO.	LOGIC OR OPTION SERIAL NO.'S AFFECTED	FIELD CODE	SYNOPSIS
8L 00056	>8L	M	DEC 69 - CHANGES A DIMENSION AND WELDING INSTRUCTIONS FOR THE CHASSIS FRAME.
G793 00001	ALL G793	F	JAN 70 - SPECIFIES A REDESIGN OF THE G793 WHICH WILL SEPARATE THE RESISTORS FOR BETTER HEAT DISSIPATION. HOT SPOTS OCCUR ON G793'S IN PDP-8I AND PDP-8L BECAUSE OF THEIR POSITION ABOVE THE INHIBIT RESISTORS. G793 CIRCUIT SCHEMATIC REVISION A
8L 00057	AS ORDERED	D	JAN 70 - ADDS A CUSTOMER-REQUESTED VARIATION TO THE INDICATOR PANEL.
8L 00058	8L 1-2479> WITH M8L	F	JAN 70 - CORRECTS FAILURE OF "RMF" TO RESTORE MEMORY FIELD IF AN INTERRUPT OCCURS DURING AN IOT CYCLE WHILE THE PROGRAM IS RUNNING IN UPPER MEMORY. (0\$5.00 0\$3.00 0\$320.00)
8L 00059	8L 2636>	F	JAN 70 - ADDS GROUNDS TO THE I/O CONNECTOR SLOTS TO REDUCE I/O LINE NOISE. THIS IS A DESIGN ECO WHICH HAS NOT BEEN IDENTIFIED AS THE SOLUTION TO ANY SPECIFIC FAILURE. (0\$5.00, 0\$3.00, 0\$315.00)

THIS PAGE
 CONTINUED AND OR
 ENLARGED BELOW

PDP8L-DOL-17

BL 00090	BL 100-2516>	F	JAN 70 - TIES A FLOATING LEG OF THE M706 AT CD32 TO +3 VDC AND REMOVES -3 VDC FROM THE W076 CONNECTOR TO ELIMINATE RANDOM FAILURES WHICH OCCUR WHEN AN EXTRA CHARACTER IS ADVANCED. THE PROBLEM IS ESPECIALLY NOTICEABLE WITH TELETYPE TEST, PART 2, TEST 3.
BL 00091	BL 2597> RETROFIT IF POSITIVE BUS OPTIONS ARE INSTALLED	F	FEB 70 - ADDS A G717 PULSE TERMINATOR MODULE TO THE BL PARTS LIST SO THAT IT WILL BE AVAILABLE WHEN OPTIONS ARE ADDED TO THE BL I/O BUS. (*\$5.00, **\$4.00, ***10.00)

<p align="center">LEGEND</p> <p>FIELD CODE</p> <p>F - Field action may be required D - Design ECO P - Print or Wire List change M - Mechanical ECO</p> <p>SYMBOL</p> <p>> - ECO applicable to future production</p> <p>ECO CHARGES</p> <p>Changes are coded within the system. (*\$X,**\$Y,***\$Z) \$X - Charge for space and updated price only \$Y - Charge for necessary parts only \$Z - Charge for on site labor & installation by DEC</p> <p>NOTE: Changes are coded (\$**\$Y+\$Z) on the drawing by DEC</p>	<p align="center">MASTER DRAWING LIST REVISIONS</p> <table border="1"> <thead> <tr> <th>REV</th> <th>ECO NUMBER</th> <th>REV</th> <th>ECO NUMBER</th> </tr> </thead> <tbody> <tr> <td>AT</td> <td>BL-00088</td> <td></td> <td></td> </tr> <tr> <td>AU</td> <td>BL-00089</td> <td></td> <td></td> </tr> <tr> <td>AV</td> <td>BL-00090</td> <td></td> <td></td> </tr> <tr> <td>AW</td> <td>BL-00091</td> <td></td> <td></td> </tr> </tbody> </table>	REV	ECO NUMBER	REV	ECO NUMBER	AT	BL-00088			AU	BL-00089			AV	BL-00090			AW	BL-00091			<p align="center">WIRE LIST REVISIONS</p> <table border="1"> <thead> <tr> <th>REV</th> <th>ECO NUMBER</th> <th>REV</th> <th>ECO NUMBER</th> </tr> </thead> <tbody> <tr> <td>Y</td> <td>BL-00088</td> <td></td> <td></td> </tr> <tr> <td>Z</td> <td>BL-00089</td> <td></td> <td></td> </tr> <tr> <td>AA</td> <td>BL-00090</td> <td></td> <td></td> </tr> </tbody> </table>	REV	ECO NUMBER	REV	ECO NUMBER	Y	BL-00088			Z	BL-00089			AA	BL-00090		
REV	ECO NUMBER	REV	ECO NUMBER																																			
AT	BL-00088																																					
AU	BL-00089																																					
AV	BL-00090																																					
AW	BL-00091																																					
REV	ECO NUMBER	REV	ECO NUMBER																																			
Y	BL-00088																																					
Z	BL-00089																																					
AA	BL-00090																																					

PDP-8L PAGE 9

THIS PAGE
CONTINUED FROM
FRAME ABOVE

PDP8L-DOL-18

SYNOPSIS

	BL 1630-2756 ALL WITH ECO BL-00056	F	MAR 70 - CORRECTS AN ERROR IN ECO BL-00056; TIES "TS4 (0)" TO THE I/O PC ENABLE GATE. (FCO ONLY; INCLUDED IN THE BL-00056 KIT)
	>BL	M	MAR 70 - CHANGES CASTING MATERIAL FROM ALUMINUM TO PLASTIC.
BL 00074	>BL	M	MAR 70 - CHANGES MOUNTING DEPTH OF THE SCREEN IN THE SUPER CHASSIS COVER.
BL 00095	>BL	M	MAR 70 - CORRECTS MECHANICAL DIMENSIONS FOR A MOUNTING BAR.
BL 00096	N.A.	M	MAR 70 - ADDS A CABINET CONFIGURATION DRAWING TO THE BL PRINT SET.
BL 00097	BL 100-2896	F	APR 70 - ADDS MISSING RUNS TO THE WIRE LIST.
0020 00006	ALL BL	F	APR 70 - CORRECTS AN ERROR IN ECO 0020-00003; CHANGES "SERIAL NUMBERS AFFECTED" TO "RETROFIT ALL IN-PLANT AND IN THE FIELD AND ALL FUTURE". SOME REVISIONS OF THE 0020 HAVE +3 VDC SLICE AND OTHERS HAVE +4 VDC SLICE. BOTH ARE ACCEPTABLE; HOWEVER, THEY MUST NOT BE INTERMIXED. INCLUDED IN THE 0020-00003 KIT
0021 00007	ALL BL	F	APR 70 - CORRECTS AN ERROR IN ECO 0021-00003; CHANGES "SERIAL NUMBERS AFFECTED" TO "RETROFIT ALL IN-PLANT AND IN THE FIELD AND ALL FUTURE". SOME REVISIONS OF THE 0021 HAVE +3 VDC SLICE AND OTHERS HAVE +4 VDC SLICE. BOTH ARE ACCEPTABLE; HOWEVER, THEY MUST NOT BE INTERMIXED. INCLUDED IN THE 0021-00003 KIT

THIS PAGE
CONTINUED AND OR
ENLARGED BELOW

PDP8L-DOL-19

SL 00098	SL 100-3120>	P	MAY 70 - CORRECTS A STATEMENT IN THE ENGINEERING SPECIFICATIONS TO READ "POWER CONSUMPTION IS LESS THAN 300 WATTS".
0020 00007	ALL SL	F	MAY 70 - SPECIFIES A REWORKING OF THE BOARD TO ELIMINATE EXCESSIVE NOISE WHICH IS ASSOCIATED WITH THE REVISION H EYELET BOARDS. PROVIDES INSTRUCTIONS FOR FIELD REWORKING OF THE BOARDS. 6020 CIRCUIT SCHEMATIC REVISION J
SL 00099	>SL	M	MAY 70 - CORRECTS MECHANICAL DIMENSION ERRORS ON THE SUPER COVER.
SL 00100	>CMBL	P	JUN 70 - CHANGES THE CMBL CARD READER SPECIFICATION FROM 80 COLUMN TO 40 COLUMN CARDS.

LEGEND

FIELD CODE

- F - Field action may be required
- D - Design ECO
- P - Print or Wire List change
- B - Mechanical ECO

SYMBOL

- > - ECO applicable to future production

ECO CHANGES

Changes are coded within the acronym. (*BX,**BY,***BZ)

- BX - Change for space and updated price only
- BY - Change for assembly strategy
- BZ - Change for on the line only, installation by DSC

NOTE: Changes are coded BX+BY+BZ - I will on the change for ECO installation by DSC

MASTER DRAWING LIST REVISIONS			
REV	ECO NUMBER	REV	ECO NUMBER
AY	SL-00092		
AZ	SL-00093		
BA	SL-00096		
BB	SL-00097		

WIRE LIST REVISIONS			
REV	ECO NUMBER	REV	ECO NUMBER
AB	SL-00092		
AC	SL-00097		

THIS PAGE
CONTINUED FROM
FRAME ABOVE

PDP8L-DOL-20

Engineering Change Order Log

is issued and published continuously in conjunction with the
 issuance of ~~Engineering Change Orders~~ **Engineering Change Orders** for all DEC products and are available
 upon request from:

**DIGITAL EQUIPMENT CORPORATION
 FIELD SERVICE INFORMATION CENTER
 MAYNARD, MASSACHUSETTS 01754**

**EQUIPMENT
 CORPORATION**
MAYNARD, MASSACHUSETTS

ECO SYNOPSIS FOR LOGIC OR OPTION

PDP-8/L PROCESSOR LOGIC **PDP-8/L**

PRODUCT LINE	PUBLICATION DATE OF THIS SYNOPSIS PAGE	PAGE REVISION
PDP-8/L	OCTOBER 1971	8

ECO NO.	LOGIC OR OPTION SERIAL NO.'S AFFECTED	FIELD CODE	SYNOPSIS
8L 00101	N.A.	P	AUG 70 - CORRECTS THE DRAWINGS FOR THE BEZEL AND THE BEZEL INDICATOR PANEL.
8L 00102	>8L	M	AUG 70 - SPECIFIES THE USE OF AN ALUMINUM CASTING BECAUSE OF THE EXCESSIVE FLEXIBILITY OF THE PLASTIC BEZEL.
8L 00103	>8L	M	SEP 70 - SPECIFIES THE USE OF VARIATIONS OF THE STANDARD BLANK PANEL FOR THE 8L.
8L 00104	>8L	M	OCT 70 - SPECIFIES THE USE OF SCOTCHWELD #2216 TO REPLACE MARBOND ADHESIVE WHICH IS NO LONGER AVAILABLE.
8L 00105	N.A.	P	OCT 70 - ADDS IN-PLANT TEST AND ACCEPTANCE PROCEDURES TO THE CMPL PRINT SET.
8L 00106	8L 100-3124 502-5051 RETROFIT 8L WITH DF32-D OR TC05	F	OCT 70 - ADDS TERMINATION FOR THE 1/2 BJS; AN 4902YA FOR THE MB BITS AND AN 4902YA FOR THE AC BITS. (*\$5.00, **\$25.00, ***\$5.00)

THIS PAGE
CONTINUED AND OR
ENLARGED BELOW

PDP8L-D0L-21

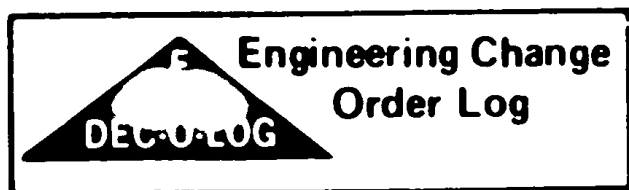
8L 00107	V.A.	F	DEC 70 - PROVIDES A MINOR ADDITIONAL PRINT CORRECTION TO ECO 9L-00062. (**S.00, **NONE, ***S.20 INCLUDED IN THE 9L-00059 KIT)
8L 00108	>8L	M	DEC 70 - SPECIFIES A CHANGE FROM SAND CASTING TO DIE CASTING OF THE 9L 3E2EL.
9L 00109	>8L	D	JAN 71 - CHANGES THE WIRING OF 91 LOGIC PANELS TO ACCOMMODATE THE FEED HOLE STORED PCB4 WHEN IT BECOMES AVAILABLE. THIS ECO WILL NOT BE FIELD IMPLEMENTED; IDENTICAL FIELD MODIFICATION INSTRUCTIONS ARE INCLUDED IN THE KIT FOR ECO PCB4-C0044.
9L 00110	V.A.	P	FEB 71 - ORDERS CHANGES IN THE 81 PRINT SET TO REFLECT THE WIRING CHANGES MADE TO ACCOMMODATE THE FEED HOLE STORED PCB4 (PC91, PR81). THIS ECO WILL NOT BE FIELD IMPLEMENTED; IDENTICAL INFORMATION IS INCLUDED IN THE KIT FOR ECO PCB4-C0044.

<p align="center">LEGEND</p> <p>FIELD CODE</p> <p>F - Field action may be required D - Design ECO P - Print or Wire List change M - Mechanical ECO</p> <p>SYMBOL</p> <p>> - ECO applicable to future production</p> <p>ECO CHARGES</p> <p>Charges are coded within the synopsis (**SX **SY ***SZ) SX - Charge for Space and updated prints <u>only</u> SY - Charge for necessary parts <u>only</u> SZ - Charge for on site labor <u>only</u> installation by DEC</p> <p>NOTE Charges are additive (SX+SY+SZ = Total on site charge for ECO installation by DEC)</p>	MASTER DRAWING LIST REVISIONS		WIRE LIST REVISIONS				
	REV	ECO NUMBER	REV	ECO NUMBER	REV	ECO NUMBER	REV
	8C 4150- 064				A0 9L-00109		
	8D 9L-00102						
	8E 9L-00106						
	8F 9L-00107						
	8H 9L-00103						
	8J 9L-00109						
	8K 9L-00110						

PDP-8/L PAGE 11

THIS PAGE
CONTINUED FROM
FRAME ABOVE

PDP8L-004-22



8/L

PDP-8/L Processor
Logic

1343 R638

PROCESSOR TYPE PDP-8/L

8L-00111 CODE: P
APR 71 CORRECTION 1 General operation specifications of the CM8L optical mark reader are being changed
PROBLEM 2 Optical mark data card specifications must be added
CORRECTION 2 Add card specification A-SP-CM8 L 7
CORRECTION 3 Add Incoming Inspection Procedure to prints
In plant effectivity -06 documentation change only

8L-E0112 CODE: F ML: DL
JUN 71 PROBLEM When clearing TTY KEYBOARD flag READER RUN is set, causing tape to advance This is undesirable in some programming situations
CORRECTION Clear the flag with IOP4 (read buffer) and set READER RUN with IOP2
In-plant effectivity -01 phase in
Field effectivity Retrofit only at request and expense of customer
(Time To Install And Test 5 Hour) Documentation \$ 5 00 Parts
None Kit Contents FCO/Prints)

8L-C0113 CODE: F ML: BM WL: AE
JUN 71 PROBLEM Extended address bits '0' and '1' are open collector and have no pull-up resistors when a BM8 L is installed
CORRECTION Wire in clamp loads which are available in the PDP 8/L

NOTE Kit will be included with BM8-L add-on
In-plant effectivity -03 rework immediately
Field effectivity -Only when a BM8 L memory expansion is installed
(Time To Install And Test Varies With System Configuration)
(Kit Contents FCO Only)

PAGE 12

PDP8L-DOL-23

FECO'S

ADD/DELETE SHEET

SHEET 2 OF 3

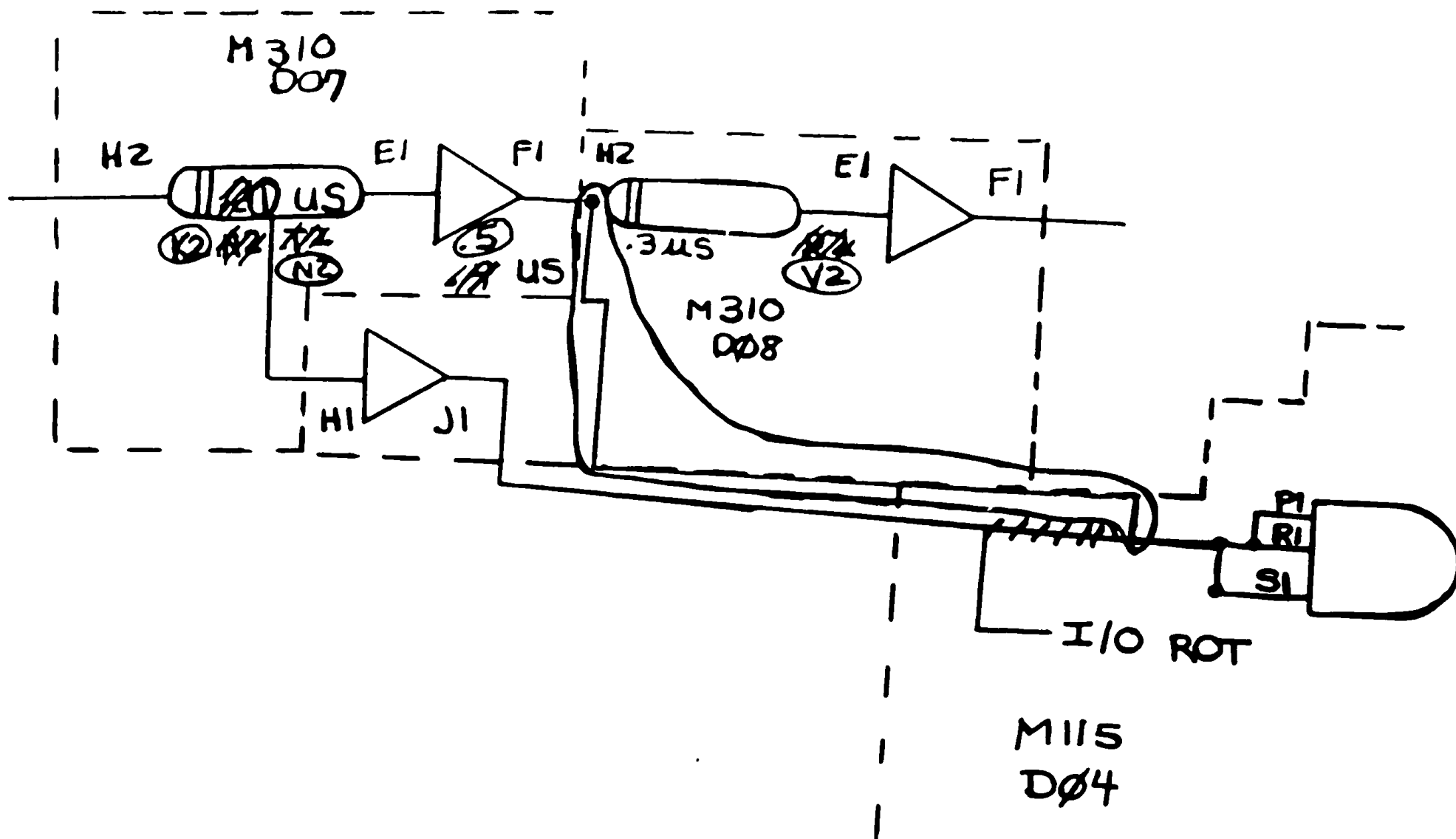
MAKE ALL DELETIONS FIRST

PAGE 1 OF 1

COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	D07E1	D07E1	D07T2			X
		D07E1	D07N2		X	
	D07F1	D04P1	D04R1		X	
		D04R1	D04S1		X	
		D04S1	D07F1		X	
	D07N2	D07N2	D08H1			X
	D07K2	D07K2	D08H1		X	
	D08R2	D08E1	D08R2			X
	D08V2	D08E1	D08V2		X	
	I/O ROT	D08J1	D04P1			X
		D04P1	D04R1			X
		D04R1	D04S1			X

ECO NUMBER <u>8L-00013</u> MADE BY <u>BETTY VIDITO</u>		DRAWING NUMBER AFFECTED			
		CODE	SIZE	NUMBER	NEW REV LTR
		WL	IC 8L 0 19		C

PDP8L-FCO-2



ECO #8L-00013 J-BS-8L-0-2 - NEW REV. C

COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	BEMA	B35T2	C1ØT2		X	
		C1ØT2	C26P2		X	
	CØ5N2	CØ5N2	DØ4V2			X
		CØ5N2	CØ6K1		X	
	C15F1	C15F1	B13E2		X	
	DØ4U1	DØ4U1	DØ4V2		X	
		DØ4U1	CØ6K1			X
	DØ7F1	DØ4P1	DØ4R1			X
		DØ4R1	DØ4S1			X
		DØ4S1	DØ7F1			X
		CØ5L2	CØ5M2		X	
		CØ5M2	DØ7F1		X	
	$\overline{\text{EMA}}$	B35V2	C26P2			X
		C26P2	C1ØT2			X
	+3V (1)	CØ2V1	CØ2J1			X
		CØ2V1	CØ2M1		X	
		CØ2M1	CØ2J1		X	
	KEY EX + DP	DØ2D1	DØ9H1			X

DRAWING NUMBER AFFECTED

ECO NUMBER 8L 00022

MADE BY ELAINE FROIAS

CODE	SIZE	NUMBER	NEW REV LTR
WL	K	PDP8 L 19	F

PDP8L-FC-5

ADD/DELETE SHEET

SHEET 3 OF 9

MAKE ALL DELETIONS FIRST

PAGE 2 OF 3

COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	KEY EX + DP	D09H1	B10M2			X
		D02D1	B10M2		X	
	KEY LA+EX+DP	C02K2	D09H1		X	
	KEY LA	C02J2	C02M1			X
		C02M1	D02A1			X
		C02J2	D02A1		X	
	MEM ALT 1	C05L2	A10T2			X
		A10T2	D04P1		X	
	MEM ALT 2	C05M2	A12T2			X
		A12T2	D04R1		X	
	MFTP 0	C02T2	C15V2		X	
	MFTP 1	C05R2	C15V2			X
		C05R2	D17L2		X	
		D17L2	D17K2		X	
	MFTP 2	D17L2	D17K2			X
		D17K2	D12A1			X

DRAWING NUMBER AFFECTED

ECO NUMBER 8L 00022

MADE BY ELAINE FROIAS

CODE	SIZE	NUMBER	NEW REV LTR
WL	K	PDP8-L-19	F

PDP8L-FCU-6

ADD/DELETE SHEET

SHEET 4 OF 9

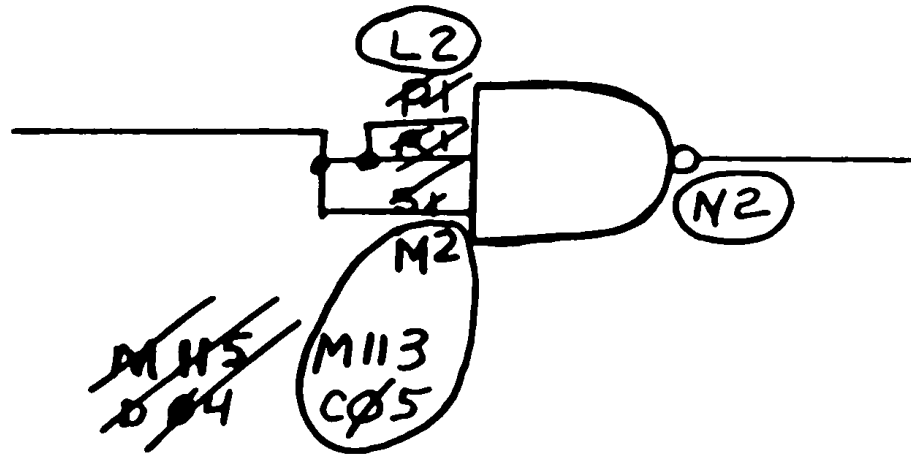
MAKE ALL DELETIONS FIRST

PAGE 3 OF 3

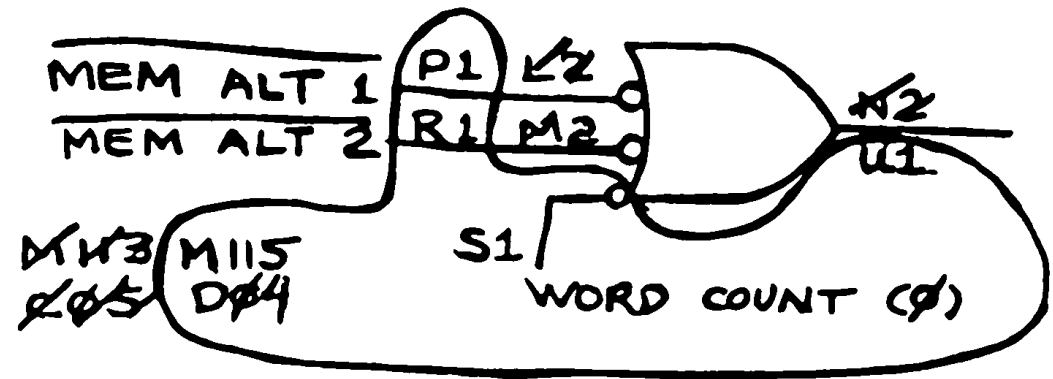
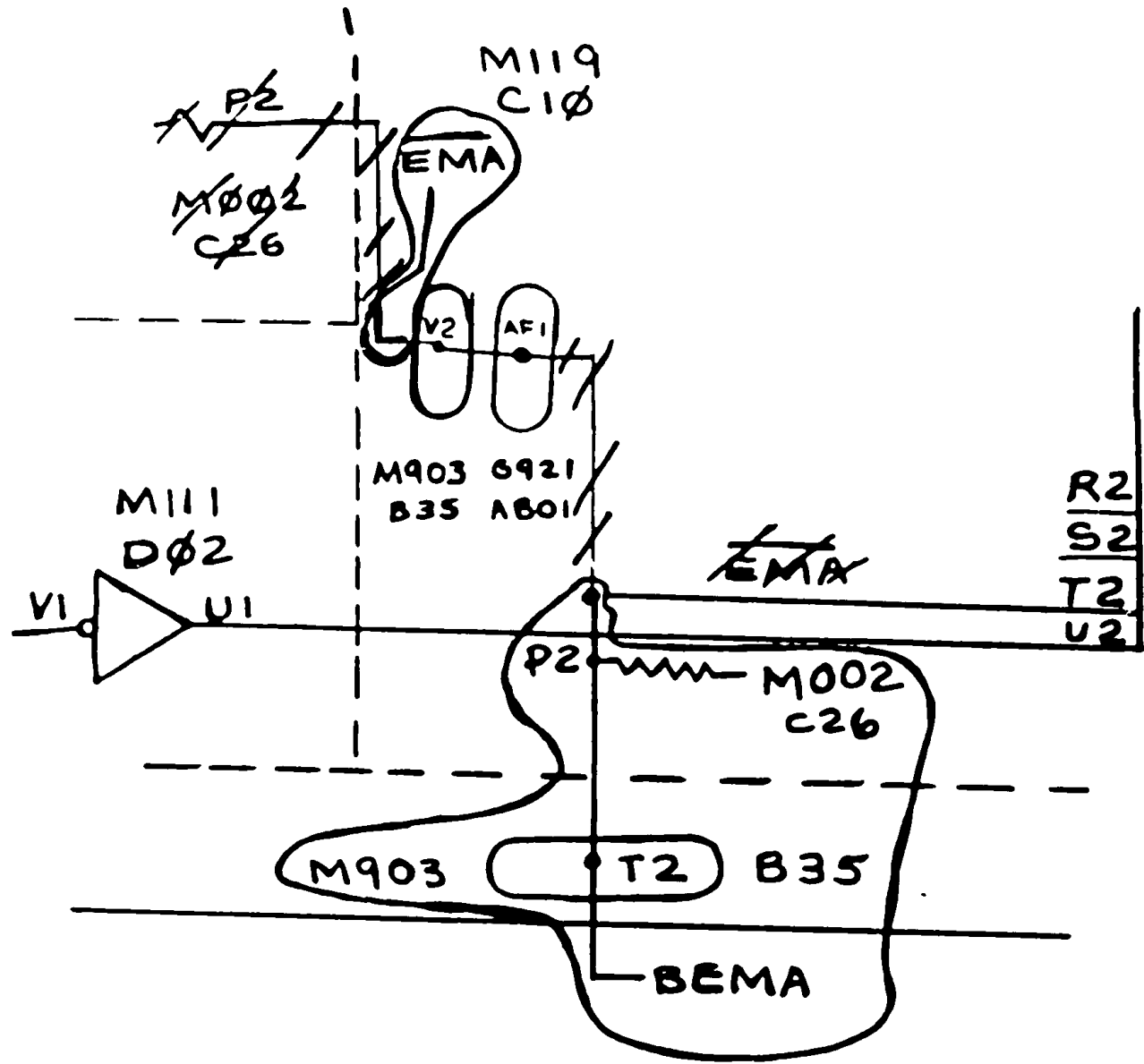
COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	MFTS Ø	DØ9J1	B13E2			X
	MFTS 1 (Ø)	CØ3K2	DØ4F1			X
		CØ3K2	C15D1		X	
		C15D1	DØ4F1		X	
	MFTS 2 (Ø)	CØ3H2	DØ4H1			X
	MFTS 2 (Ø)	C15E1	CØ3H2		X	
		C15E1	DØ4H1		X	
	WORD COUNT (Ø)	DØ2S2	D1ØR2			X
		DØ2S2	DØ4S1		X	
		DØ4S1	D1ØR2		X	

ECO NUMBER <u>8L-ØØØ22</u> MADE BY <u>ELAINE FROJAS</u>		DRAWING NUMBER AFFECTED			
		CODE	SIZE	NUMBER	NEW REV LTR
		WL	K	PDP8 L 19	F

PDF8L-FCO-7

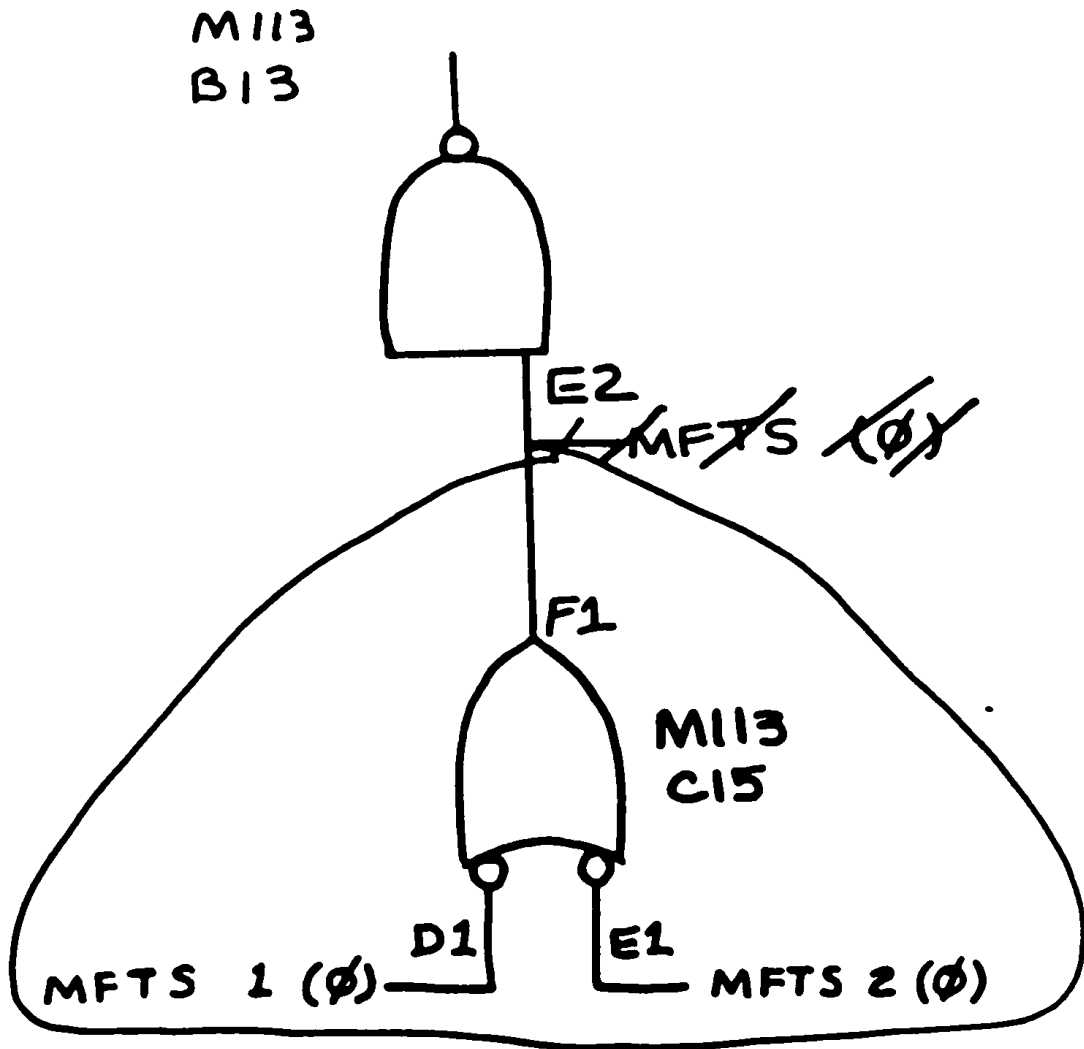


ECO#81-00022 J-BS-PDP8-L-2 NEW REV D



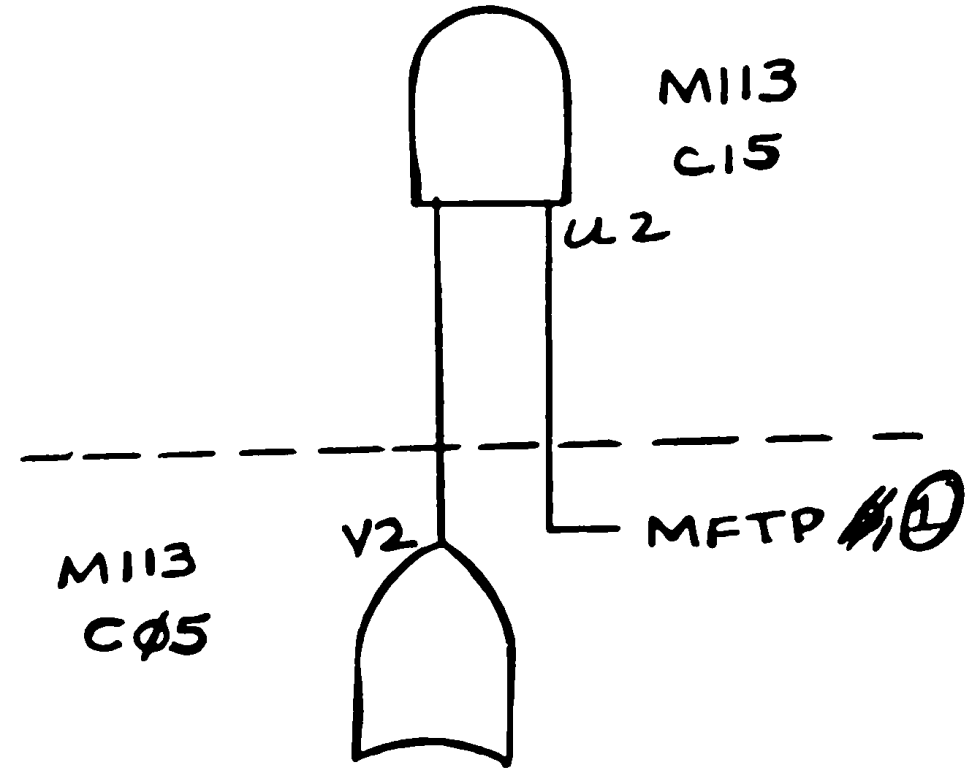
ECO#81-00022 J-BS-PDP8-L-3 NEW REV B

PDP8L-FCO-8



DWG LOC A-4

ECO #8L-00022 D-BS-PDP8-L-4 NEW REV. B



MI13 C05

MI13 C15

DWG. LOC. B5

M617 D17

K2, L2

LP2

M2, N2

DWG. LOC. C2

KEY LA

MFTP #1

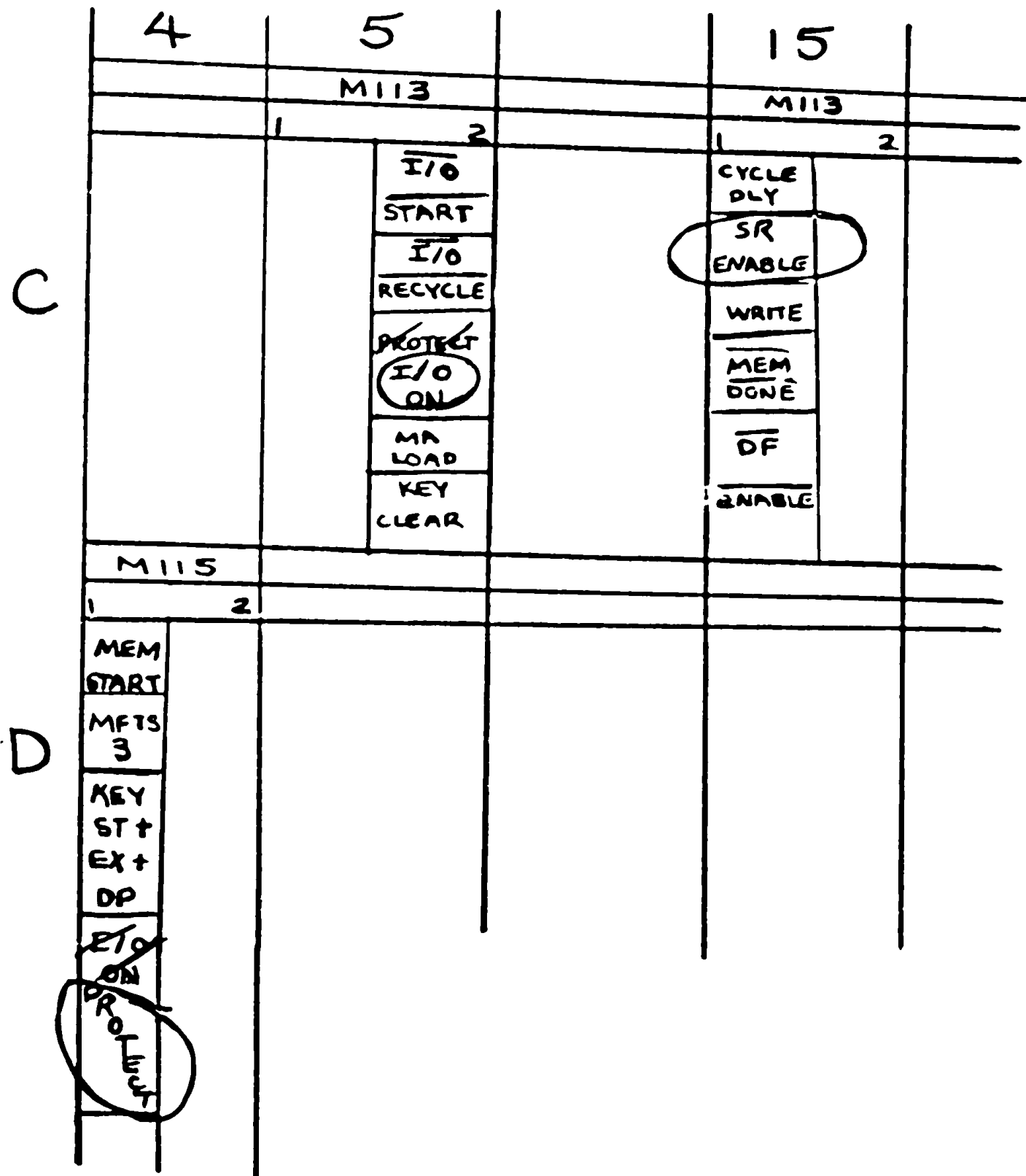
ECO #8L-00022

D-BS-PDP8-L-17

NEW

REV. B

PDP8L-FCO-9



ADD/DELETE SHEET

SHEET 2 OF 3

MAKE ALL DELETIONS FIRST

PAGE 1 OF 2

COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	A21C2	A21C2	A18D2			X
	A21D2	A21D2	A18E2			X
	A21E2	A21E2	A18L1			X
	A21F2	A21F2	A18M1			X
	A21H2	A21H2	A19D2			X
	A21J2	A21J2	A19E2			X
	A21K2	A21K2	A19L1			X
	A21L2	A21L2	A19M1			X
	A21M2	A21M2	A20D2			X
	A21N2	A21N2	A20E2			X
	A21P2	A21P2	A20L1			X
	A21R2	A21R2	A20M1			X
	A21S2	A21S2	A17D2			X
	A21T2	A21T2	A17E2			X
	B21C2	B21C2	B18D2			X
	B21D1	B21D2	B18E2			X
	B21E2	B21E2	B18L1			X

ECO NUMBER <u>8L-00027</u> MADE BY <u>I. TOMPKINS</u>	DRAWING NUMBER AFFECTED			
	CODE	SIZE	NUMBER	NEW REV LTR
	WL	K	8L-019	H

PDP8L-FCO-12

ADD/DELETE SHEET

SHEET 3 OF 3

MAKE ALL DELETIONS FIRST

PAGE 2 OF 2

COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	B21F2	B21F2	B18M1			X
	B21H2	B21H2	B19D2			X
	B21J2	B21J2	B19E2			X
	B21K2	B21K2	B19L1			X
	B21L2	B21L2	B19M1			X
	B21M2	B21M2	B20D2			X
	B21N2	B21N2	B20E2			X
	B21P2	B21P2	B20L1			X
	B21R2	B21R2	B20M1			X

ECO NUMBER <u>8L-00027</u> MADE BY <u>I. TOMPKINS</u>	DRAWING NUMBER AFFECTED		
	CODE	SIZE	NUMBER
	WL	K	8L-0-19
			NEW REV LTR
			H

PDP8L-FCO-13

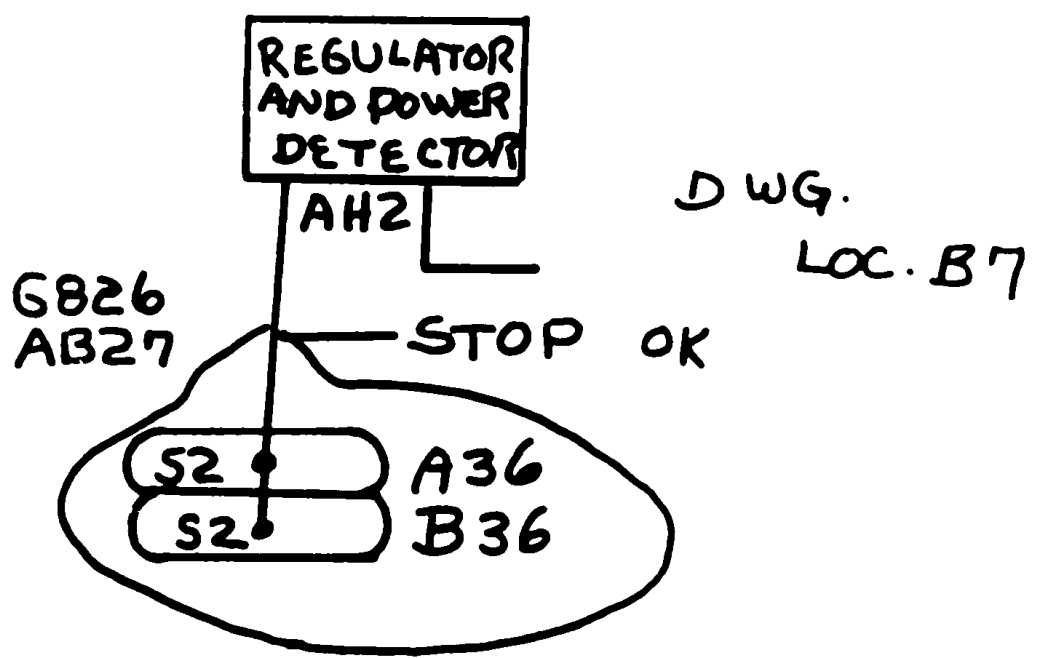
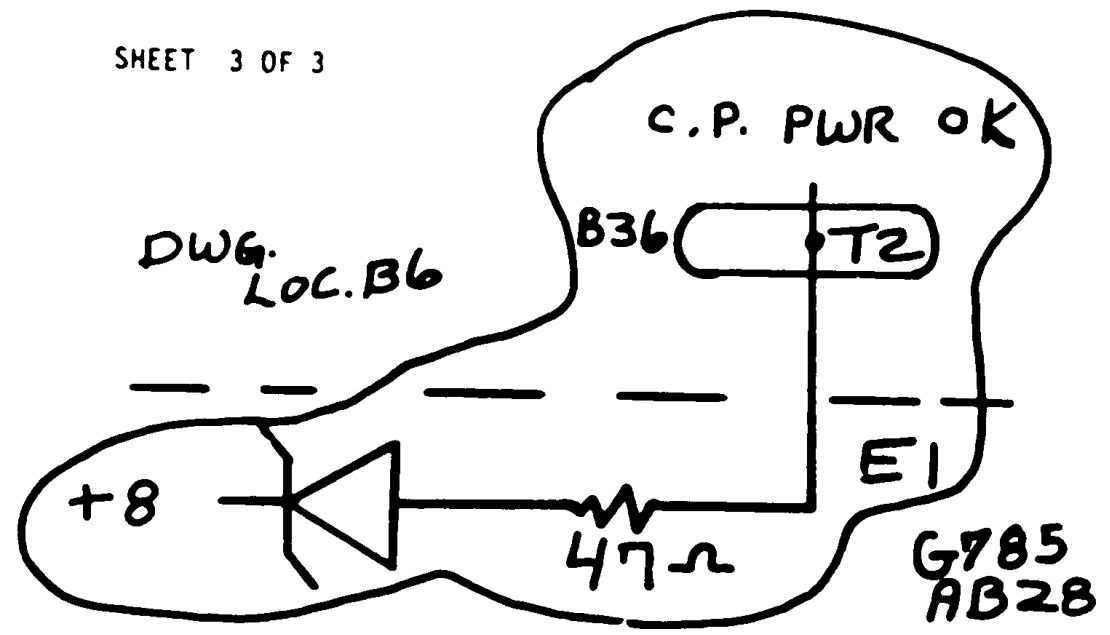
ADD/DELETE SHEET
MAKE ALL DELETIONS FIRST

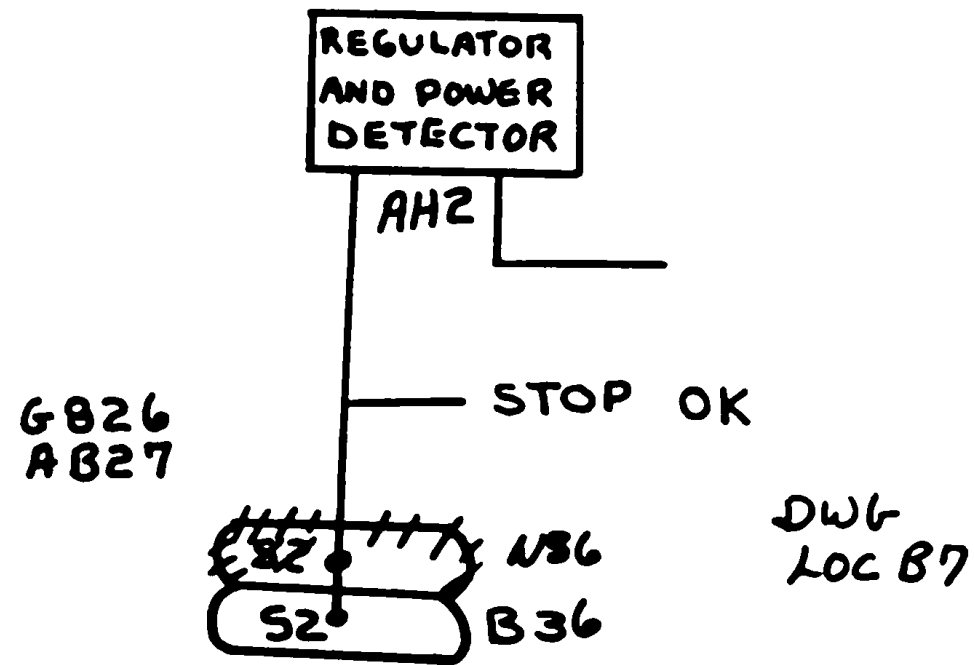
SHEET 2 OF 2
PAGE 1 OF 2

COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	CP PWR OK	A28E1	B36T2		X	
	STOP OK	A36P2	D09K1			X
		A36P2	A36S2		X	
		A36S2	B36S2		X	
	↓	B36S2	D09K1		X	

ECO NUMBER <u>PL 00030</u> MADE BY <u>BV/AV</u>		DRAWING NUMBER AFFECTED			
		CODE	SIZE	NUMBER	NEW REV LTR
		WL	K	8L-0-19	J

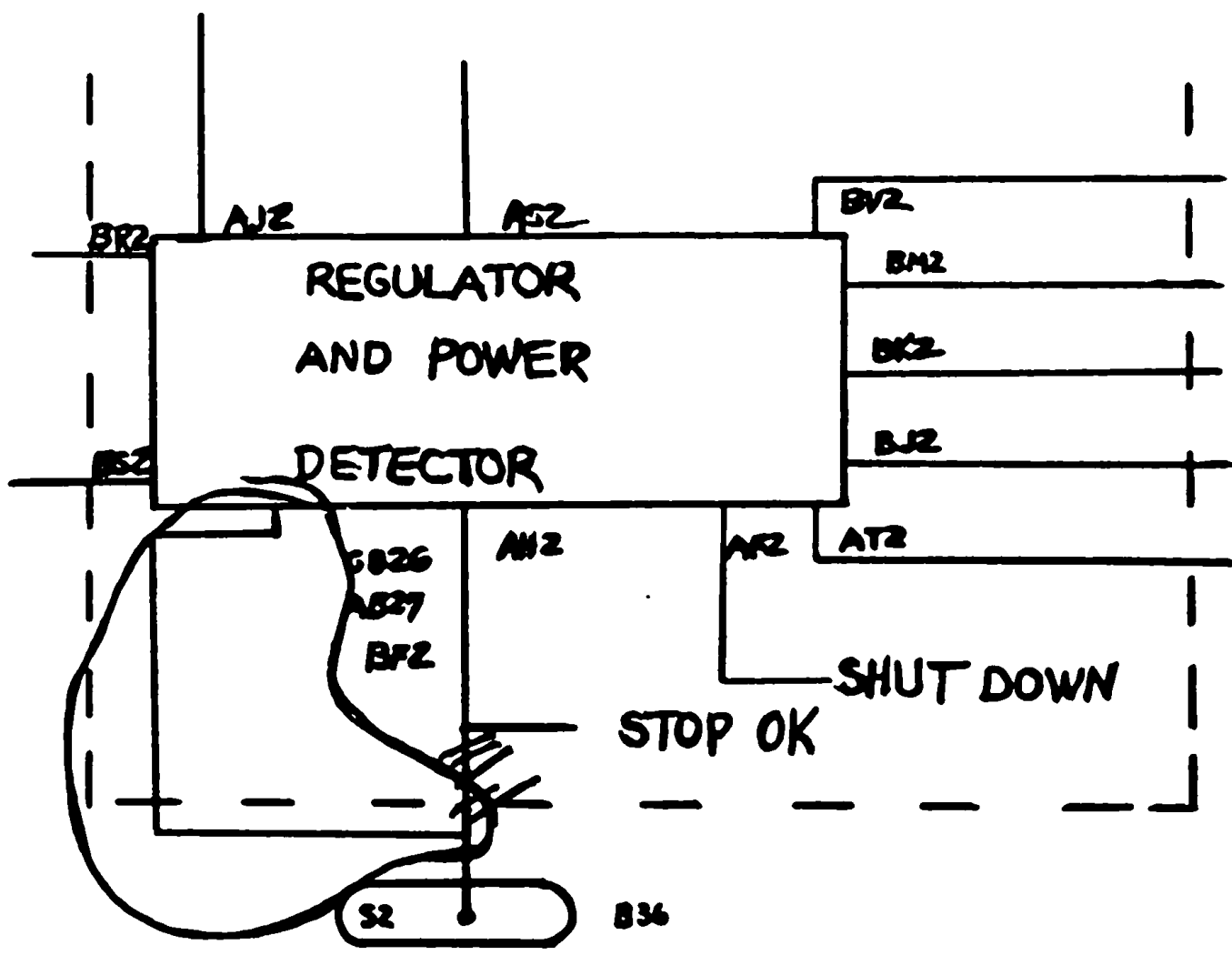
PDP8L-FCO-15





ECO #8L-00031 J-35-8L-0-13 NEW REV. D

PDP8L-FCO-19



ADD/DELETE SHEET

SHEET 2 OF 4

MAKE ALL DELETIONS FIRST

PAGE 1 OF 1

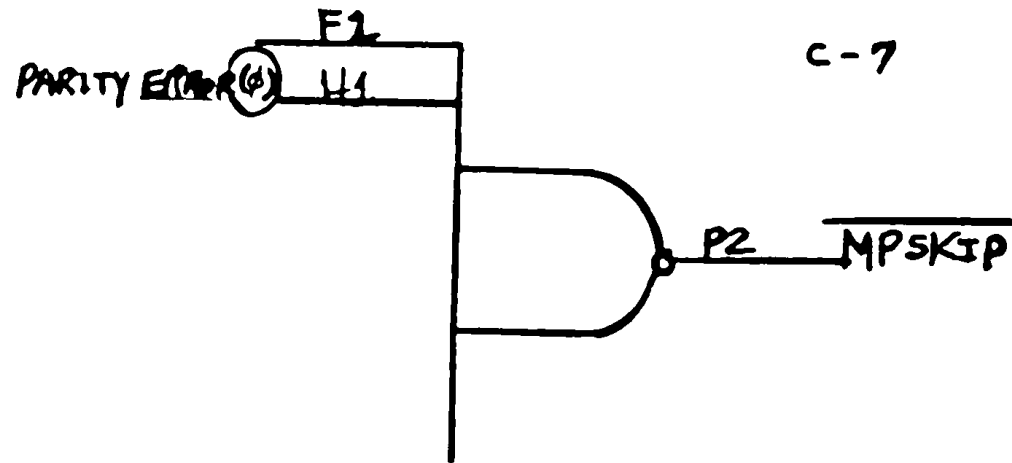
COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	PARITY ERROR	A14U1	B15H1			X
	PARITY ERROR (0)	A14P1	B15H1		X	
	A16K2	A16K2	C16P2			X
		A16K2	C16F2		X	
	MEM 06	A15S2	B04R1		X	
	MEM 07	A15T2	B04V2		X	
	PARITY ERROR (1)	A14U1	C16P2		X	

ECO NUMBER <u>8L-00044</u> MADE BY <u>J.W. PATE</u>	DRAWING NUMBER AFFECTED		
	CODE	SIZE	NUMBER
	K	WL-8L-0-19	

NEW REV
LTR

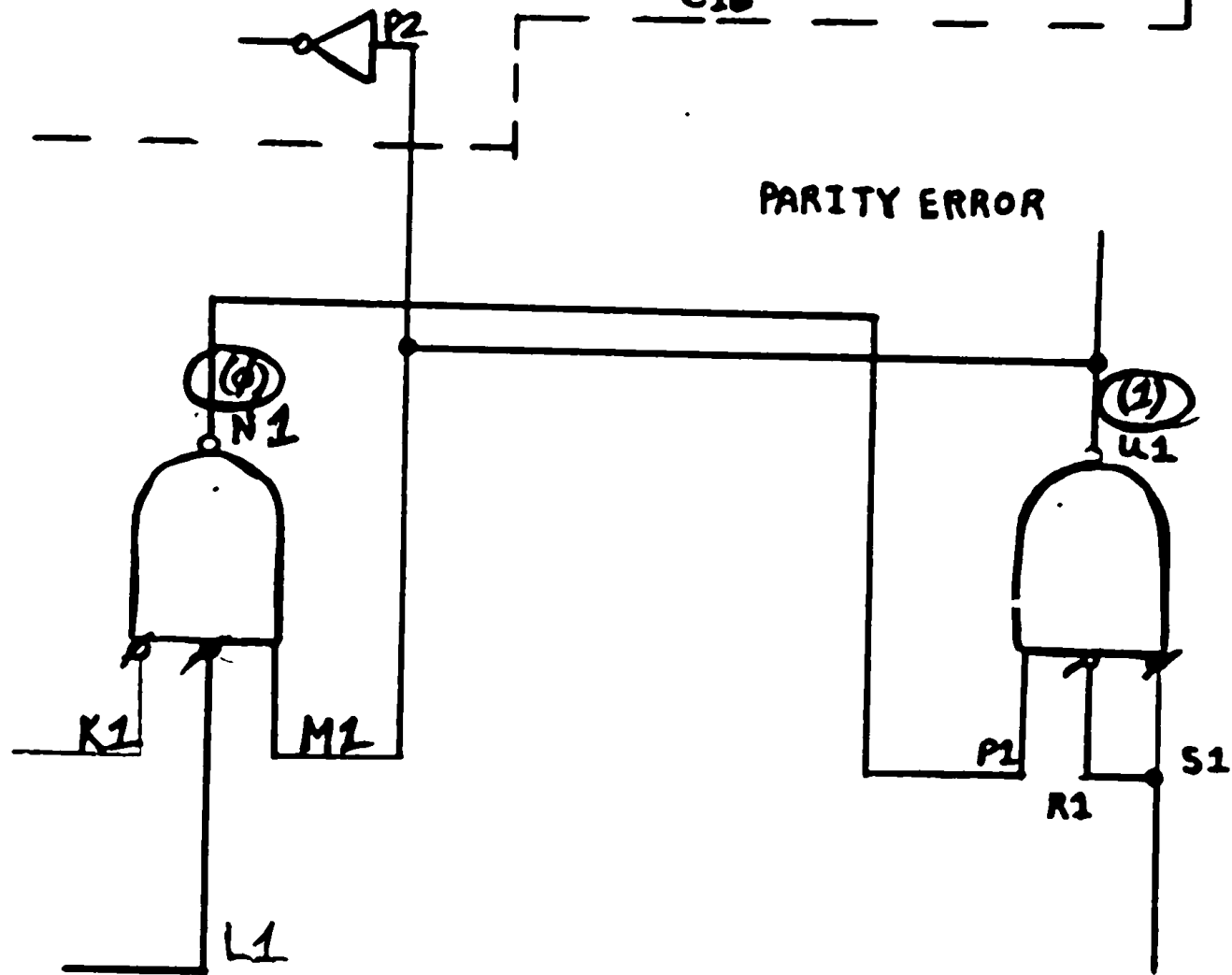
PDP8L-FCO-23

M119
B15



M112
C16

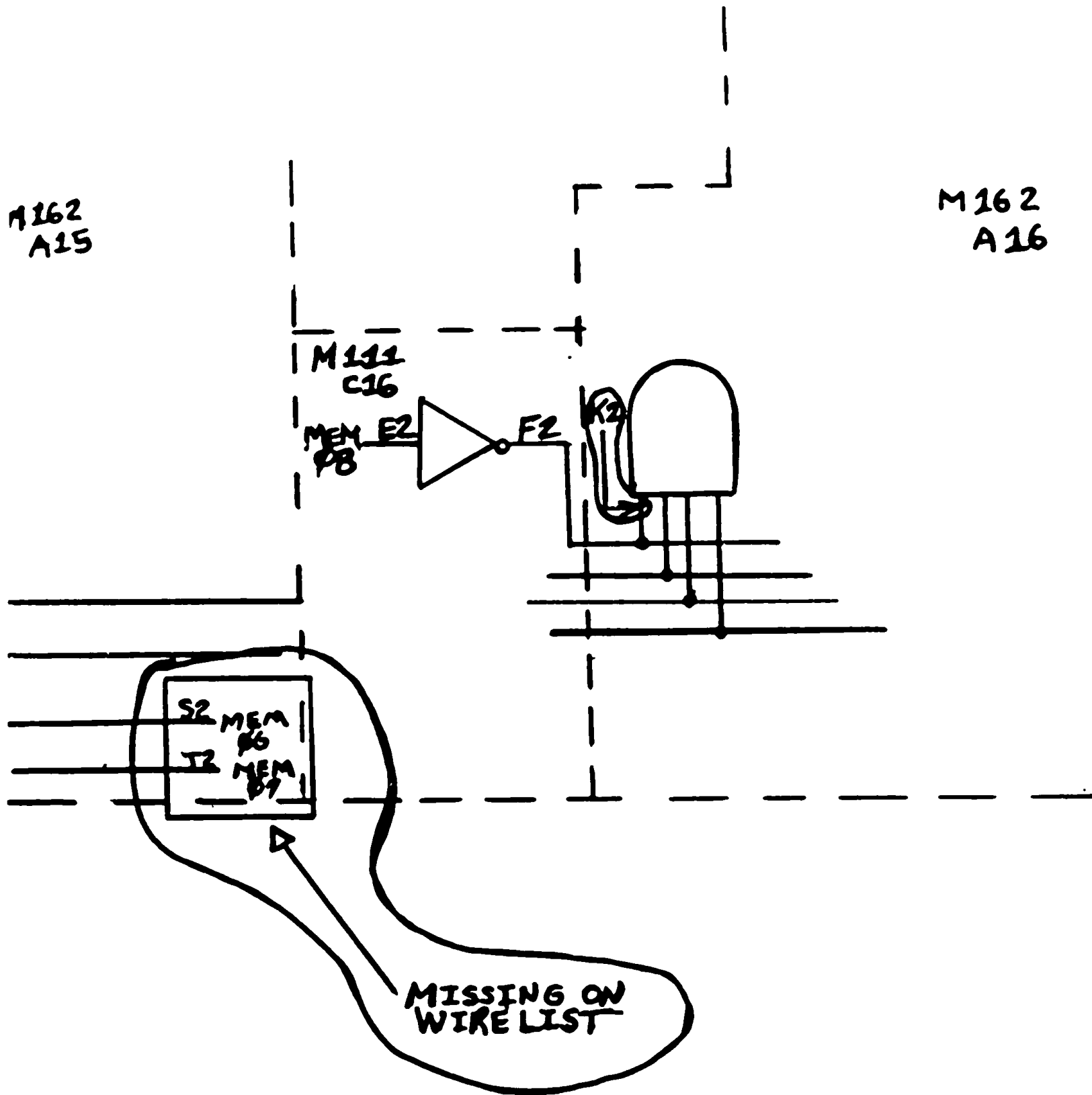
M115
A14



DWG. LOC. D-2

M162
A15

M162
A16



SHEET 2 OF 2

ADD/DELETE SHEET
MAKE ALL DELETIONS FIRST

SHEET 2 OF 8
PAGE 1 OF 2

COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	A12K1	A12K1	B13S1			X
		A12K1	A14V1		X	
	A13R1	A13R1	B08H2			X
		B08H2	B08F2			X
		B08F2	A13R1		X	
	A35E1	A35E1	B13R1		X	
	B FETCH (1)	D12J1	C14P2			X
		C14R2	C14J1			X
		C14J1	C13C1			X
		C13C1	C14R2		X	
		C14R2	D12J1		X	
	<u>INT SKIP ENABLE</u>	A08S2	B13P1			X
		B13P1	C11V2			X
		A08S2	A14T2		X	
		A14T2	C11V2		X	
	I/O ENABLE	D09J2	A35D1		X	
	<u>I/O PC ENABLE</u>	A08U2	A14U2		X	
		A14U2	B13S1		X	

DRAWING NUMBER AFFECTED

ECO NUMBER 8L 00045

MADE BY I. TOMPKINS

CODE	SIZE	NUMBER	NEW REV LTR
WL	K	8L-0-19	M

P. 8L-FCO-27

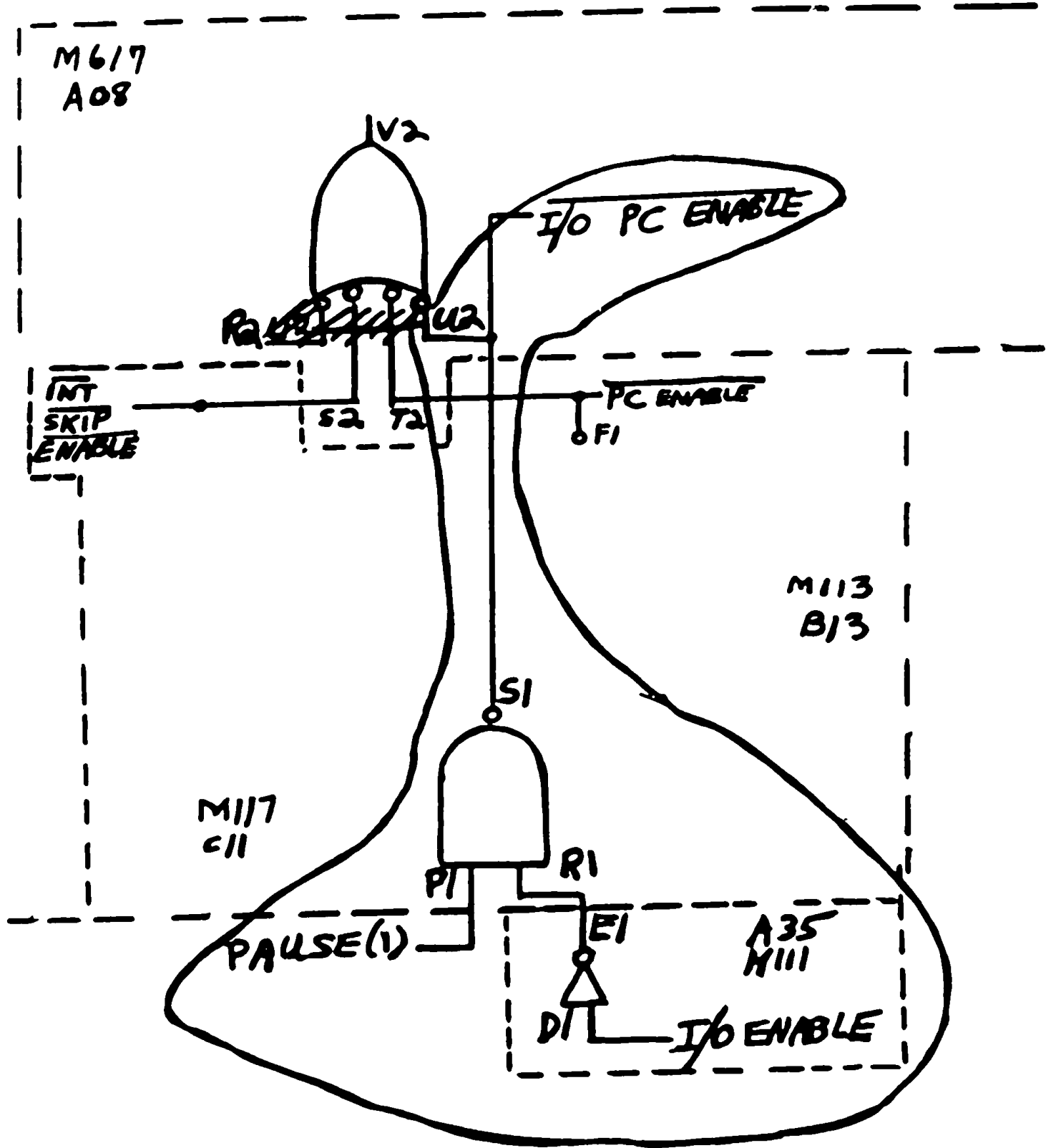
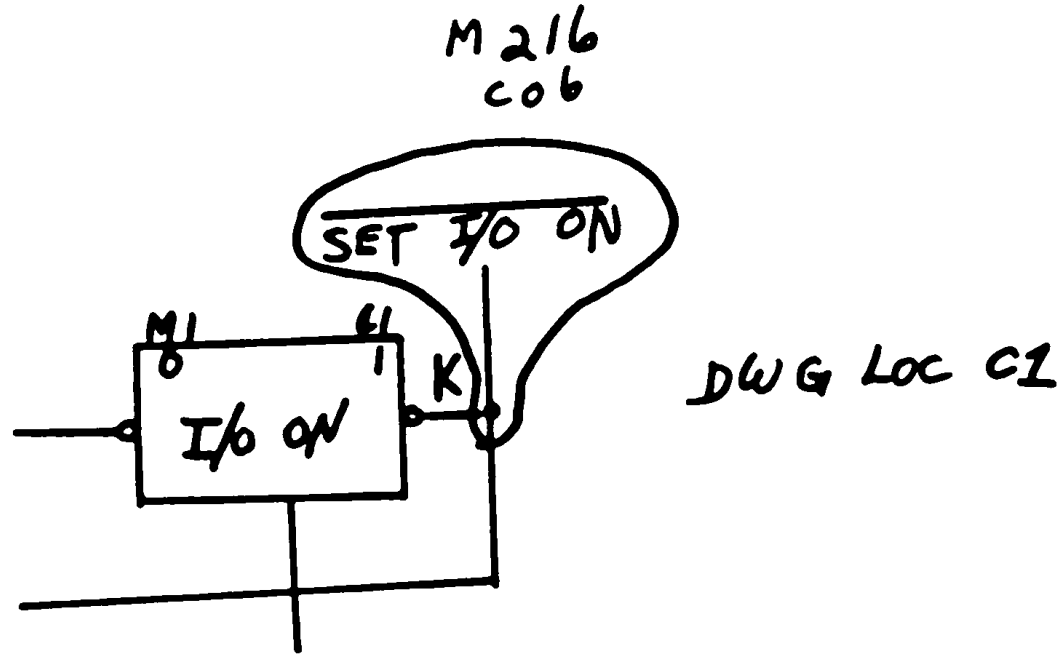
ADD/DELETE SHEET
MAKE ALL DELETIONS FIRST

SHEET 3 OF 4
PAGE 2 OF 2

COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	C12N1	C12N1	A08L2			X
		A08U2	A08R2			X
		C12N1	A08R2		X	
	PAUSE (1)	B13P1	C04L1		X	
	<u>PC ENABLE</u>	C13K1	B13R1			X
		B13R1	B13F1			X
		C13K1	A14V2		X	
		A14V2	B13F1		X	
	PC LOAD	B08J2	C14H1		X	
		C14H1	C14J1		X	
	<u>SET TO ON</u>	B08H2	C06K1		X	
	TP1	D02M1	D06H2			X
		D06H2	C14H1			X
		C14H1	B13T2			X
		B13T2	D06H2		X	
		D06H2	D02M1		X	

ECO NUMBER <u>8L-00045</u> MADE BY <u>I.T.</u>	DRAWING NUMBER AFFECTED			
	CODE	SIZE	NUMBER	NEW REV LTR
	WI	K	8L-019	M

PDP8L-FCO-28



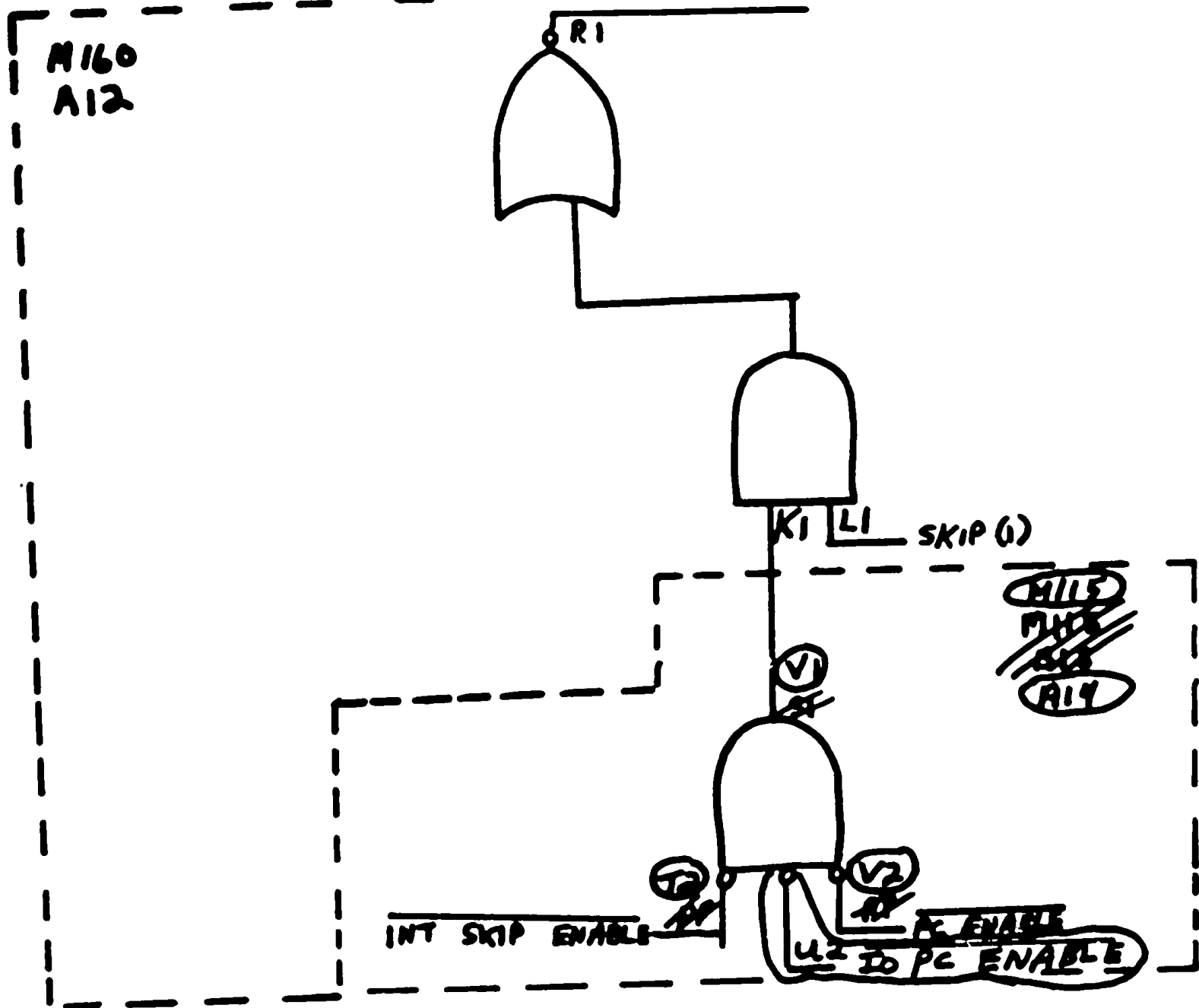
ECO-8L-00045 D-BS-8L-0-2 NEW REV E

ECO-8L-00045

D-BS-8L-0-4 C

PDP8L-FC0-29

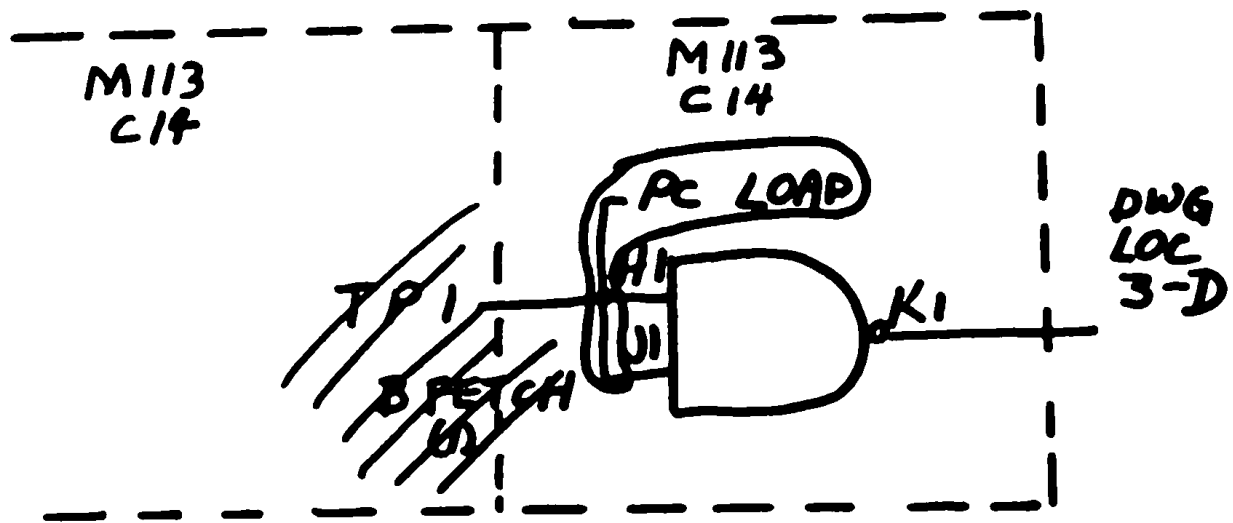
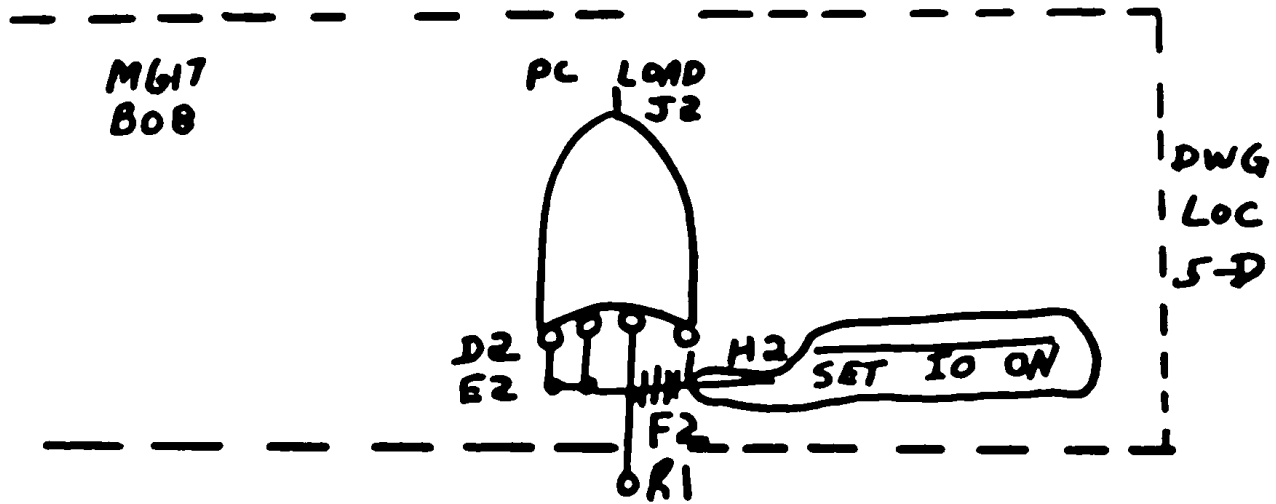
NEW
REV



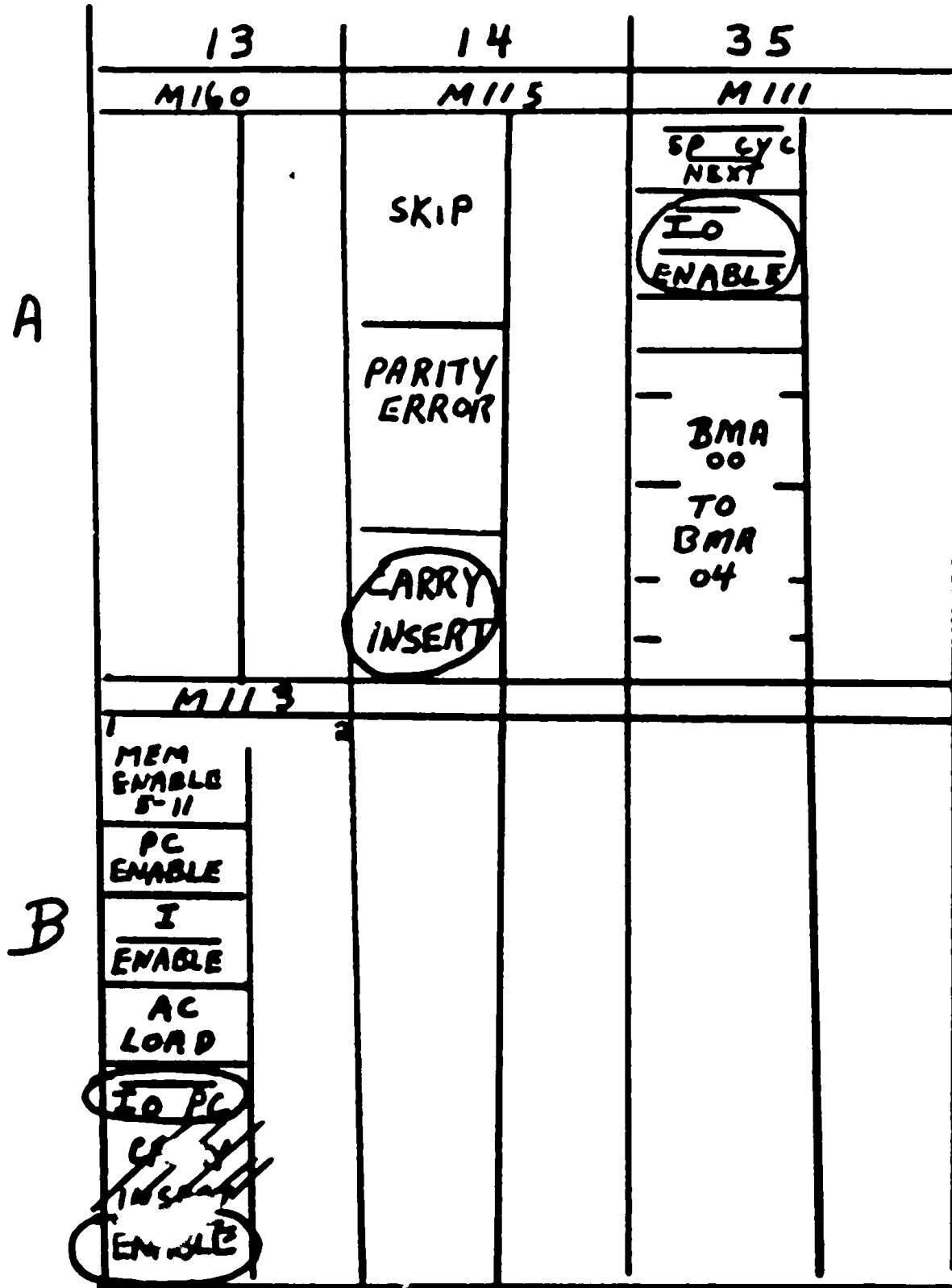
ECO-8L-00045

D-BS-8L-0-5 NEW REV. B

PDP8L-FCO-30



ECO-8L-00045 D-BS-8L-0-6 NEW REV. B



DECOD EQUIPMENT CORPORATION

PARTS LIST ENGINEERING CHANGE ORDER

CHANGE NO. 8L 00056						
DOCUMENTATION PROJECT NUMBER						
ACT	PROD. LINE	TYPE OR DISCRETE				
	2 7	0	7	1	7	5
MANUFACTURING PROJECT NUMBER						
ACT	PROD. LINE	TYPE OR DISCRETE				

STOCK DISPOSITIONS CODE	ACTION TAKEN
SC=SCRAP RN=RETAIN	PARTS ADDED
RK=REWORK NC=NO CHANGE	NPO=NO PARTS ON ORDER
	POR=PARTS ON ORDER
	IS=PARTS IN STOCK

PRODUCTION ENGINEER H. GODFREY
 DESIGN ENGINEER M. ARSENAULT

PARTS DELETED

ITEM NO.	PART NO.	NO. DEL. PER UNIT	STOCK DISP.	DESCRIPTION

PARTS ADDED

ITEM NO	PART NO	NO REQD PER UNIT	DESCRIPTION	ACTION TAKEN
1	M115		8 3 INPUT NAND GATE	

PDP8L-FCO-34

ADD/DELETE SHEET

SHEET 2 OF 4

MAKE ALL DELETIONS FIRST

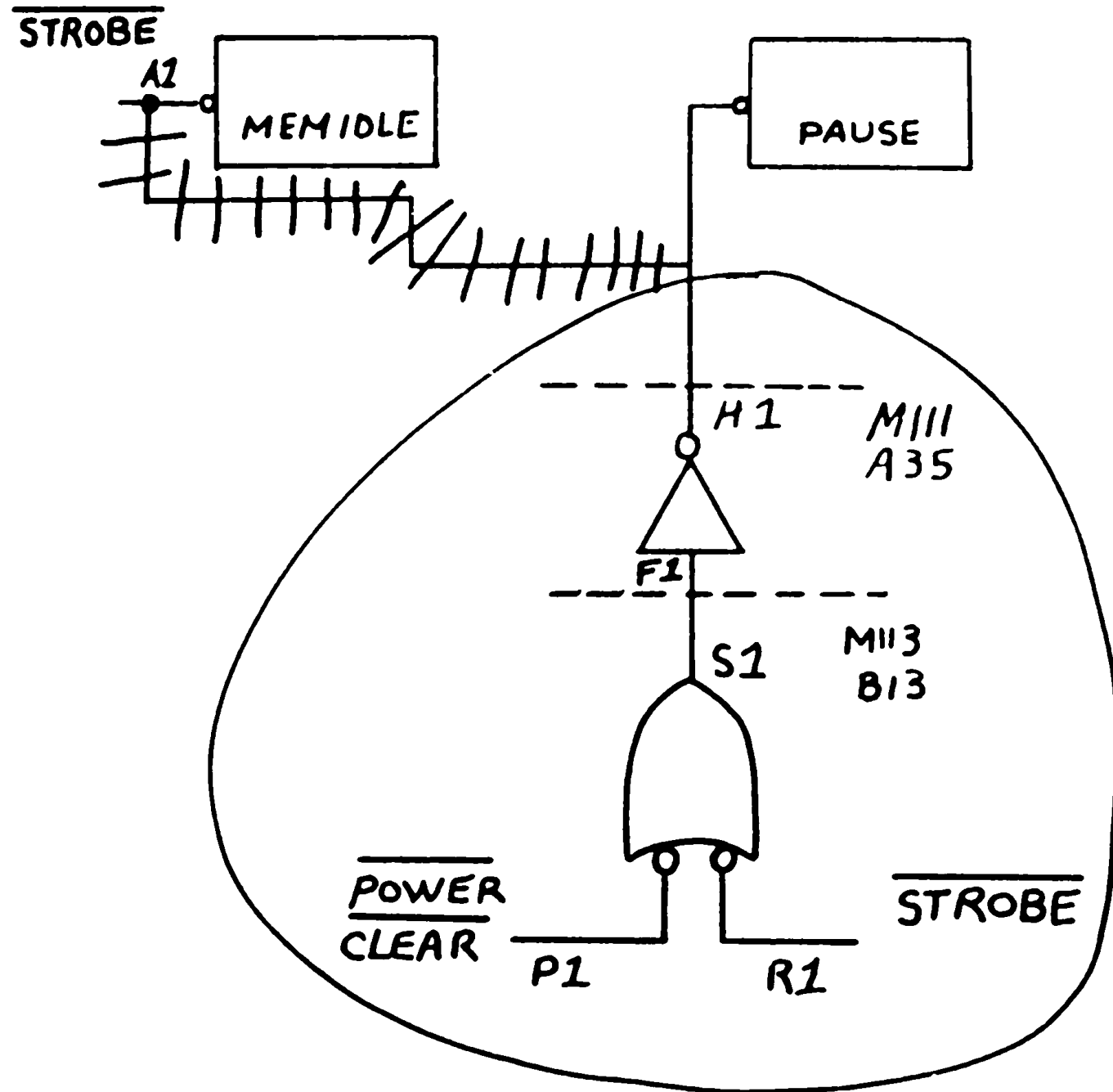
PAGE 1 OF 1

COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	B13S1	A35E1	B13S1		X	
	POWER CLEAR (~)	A27S2	D16A1			X
	↓	A27S2	B13P1		X	
		B13P1	D16A1		X	
	STROBE (~)	B13R1	B34S2		X	

ECO NUMBER <u>8L 00059</u> MADE BY <u>DAVE CHAMBERS</u>		DRAWING NUMBER AFFECTED			
		CODE	SIZE	NUMBER	NEW REV LTR
		WL	K-8L-0-19		P

PDP8L-FCO-37

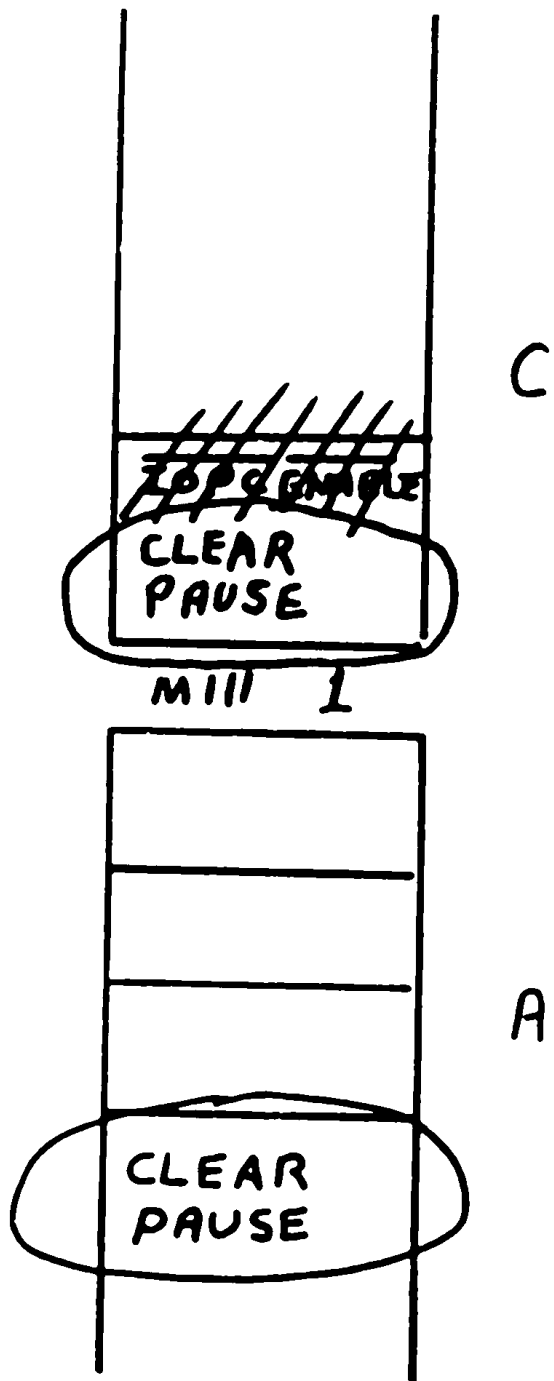
M216
CØ4



ECO 8L-00059 D-BS-8L-0-2 NEW REV. F

PDP8L-FCO-38

M113
1



ADD/DELETE SHEET

SHEET 2 OF 3

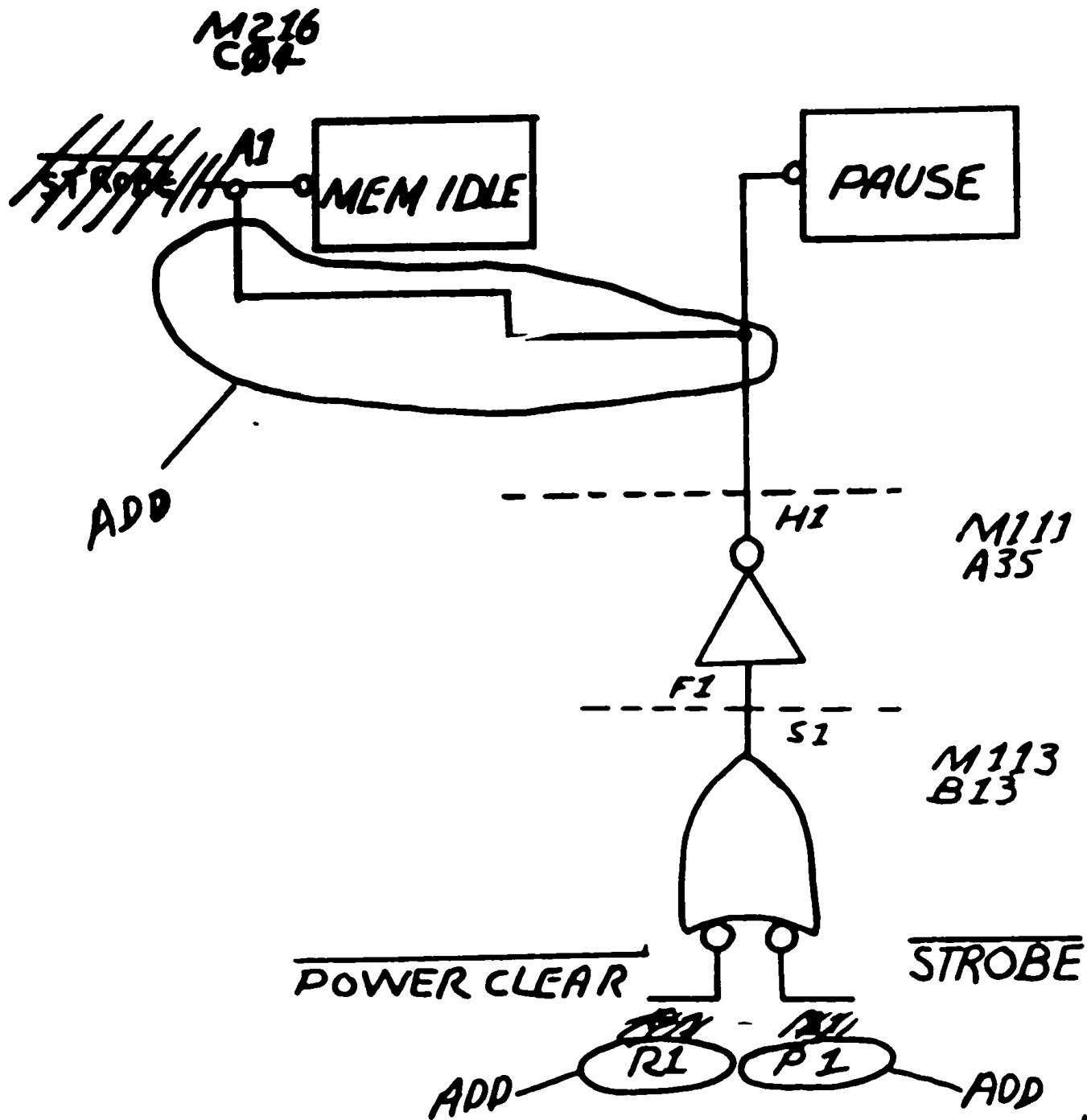
MAKE ALL DELETIONS FIRST

PAGE 1 OF 1

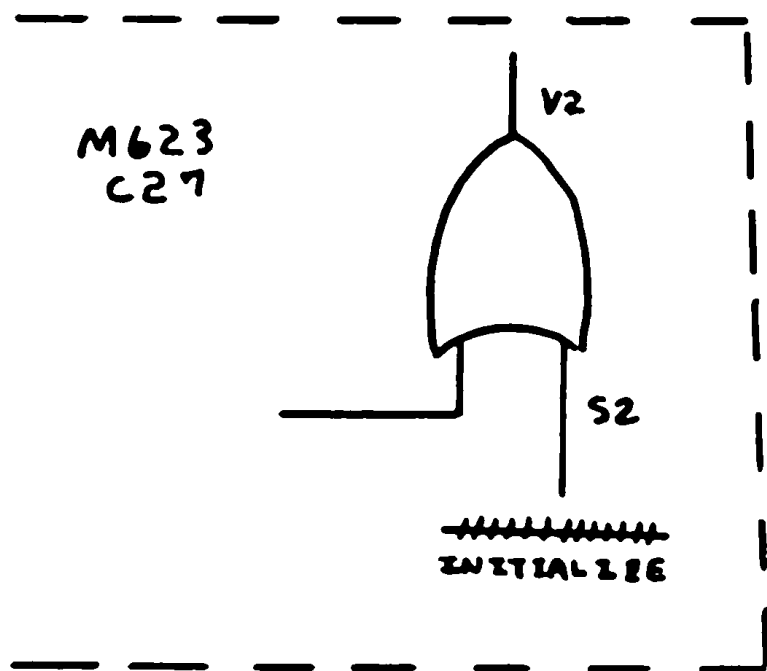
COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	A35H1	C04A1	A35H1		X	
	STROBE ~	C04N2	C04A1			X
		B34S2	B13R1			X
		C04N2	B13P1		X	
	POWER CLEAR ~	A27S2	B13P1			X
		B13P1	D16A1			X
		A27S2	B13R1		X	
		B13R1	D16A1		X	
	B13S1	B13S1	A35F1		X	

ECO NUMBER <u>8L-00062</u> MADE BY <u>DAVE CHAMBERS</u>		DRAWING NUMBER AFFECTED			
		CODE	SIZE	NUMBER	NEW REV LTR
		WL	K-8L 0-19		R

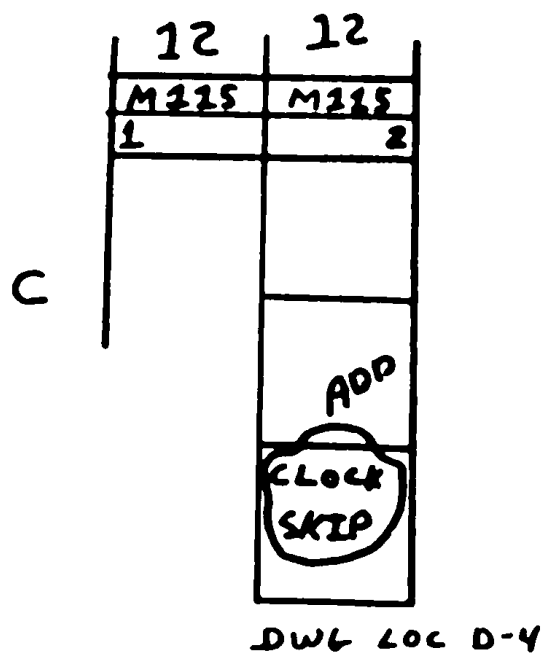
PDP8L-FCO-41



NEW REV

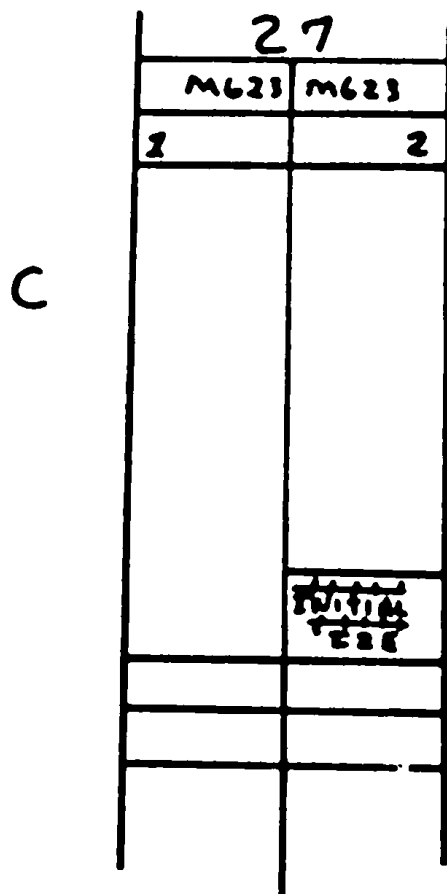


DWG LOC B-1



DWG LOC D-4

ECO 8L-00068 D-B5-BL-0-10 NEW REV C



DWG LOC B-7

POP8L-FCO-45

ECO 8L-00068 D-MU-BL-0-18 NEW REV F

A

	25	26	27	○
	B2	B2	B2	B2

B

ECO 8L-00071 D-AD-7005906-0-0 NEW REV. B

PDP8L-FCO-47

SHEET 2 OF 2

NOTES:

ADD

6 SOLDER 22 GAUGE SOLID WIRE TO THE
FOLLOWING, AS CLOSE TO BASE OF PIN AS
POSSIBLE, AFTER PANEL IS BUSSED. USE
BROWN OR BLACK SLEEVING,
A20C2 TO A23C2
B20C2 TO B23C2
C19C2 TO C23C2
D19C2 TO D23C2

DWG LOC C-182

ECO 8L-00074 D-AD-7005906-0-0 NEW REV. B

PDP8L-FCO-49

ADD/DELETE SHEET
MAKE ALL DELETIONS FIRST

SHEET 2 OF 6

PAGE 1 OF 1

COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	C14S1	B11J1	C14S1			X
	C28H2	B11J1	C28H2		X	
	F SET	B13E1	C14R1			X
		C14R1	C08M2			X
		B13E1	C28F2		X	
		C28F2	C08M2		X	
	INT ROST	A33P2	C14P1			X
		A33P2	C28D2		X	
	+3V (5)	C10V1	C14R1		X	
	KEY LA+EX+DP	C02K2	D09H1			X
		C02K2	C14P1		X	
		C14P1	D09H1		X	
	KEY LA+EX+DP	C14S1	C28E2		X	

DRAWING NUMBER AFFECTED

ECO NUMBER 8L-00078

MADE BY J.W. PATE

CODE	SIZE	NUMBER	NEW REV LTR
WL	K-8L-0-19		T

PDP8L-FCO-51

digital

A SPECIAL REVS

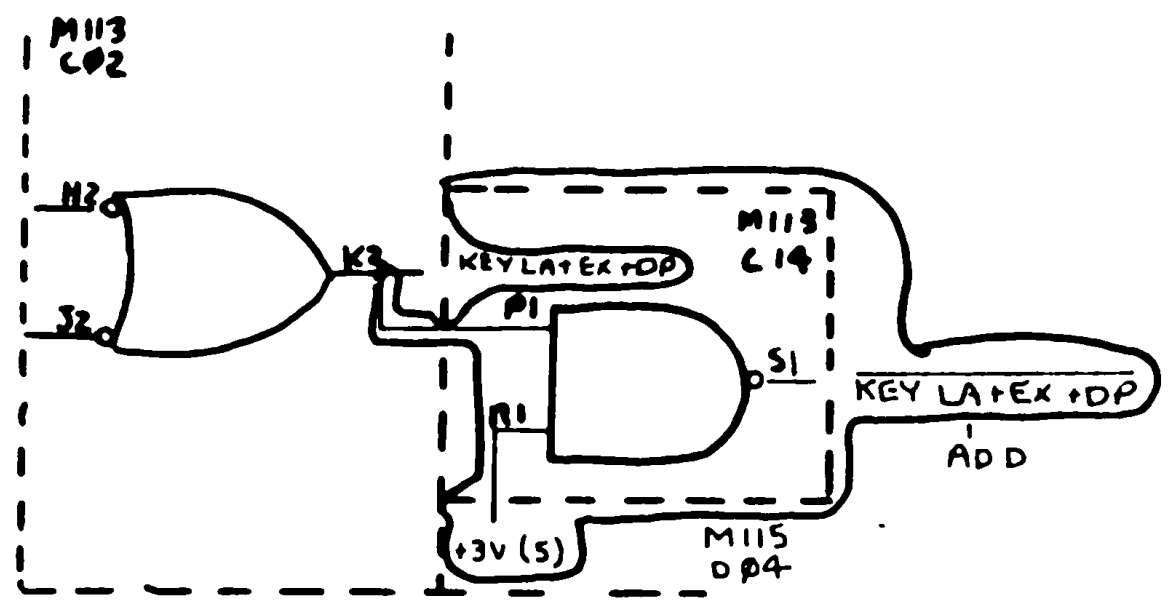
DATE: 10/17/69

ECO NO. PL 88874

PAGE 1 OF 1

DRAWING NO	NEW REV	SERIAL NO.	OLD REV ADD CIP	DEPIA?
D-BS-8L-0-2	J	1800-1799	—	
↓	K	1800----▶	J	YES
D-BS-8L-0-7	A	1-760,762-1799	—	
↓	B	1800----▶	A	YES
D-MJ-8L-0-18	F	1800-1799	—	
↓	H	1800----▶	F	YES
A-PL-8L-0-18	F	1800-1799	—	
↓	H	1800----▶	F	YES
K WL-8L-0 19	S	1800-1799	—	
↓	T	1800----▶	S	YES

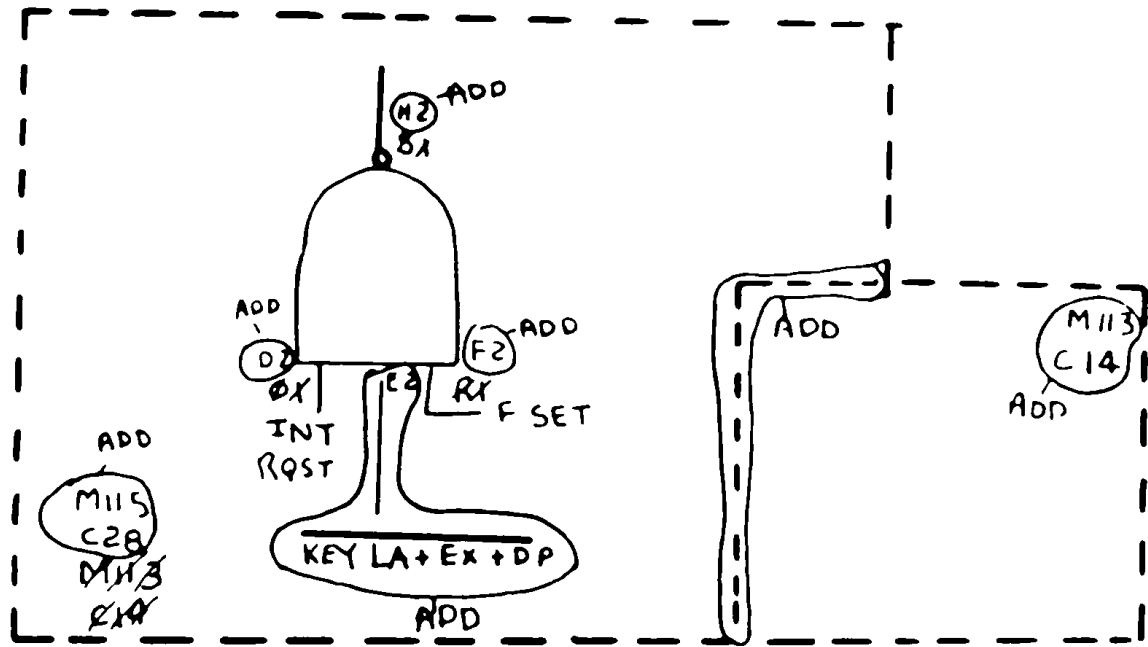
PDP8L-FCO-52



OWG LOC
B-4

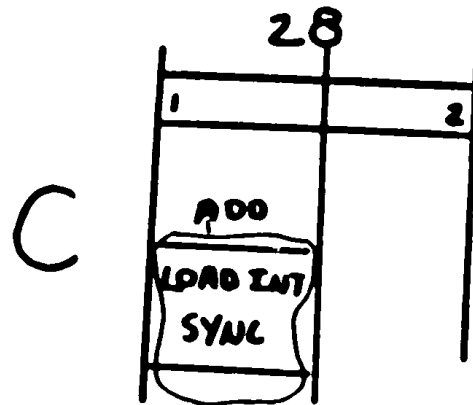
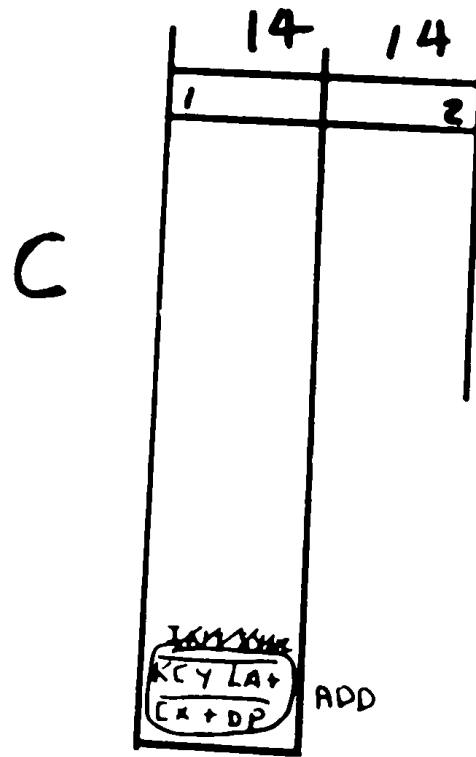
ECO 8L-00078 J-BS-8L-0-2 NEW REV K

PDP8L-FCO-53



DWG Loc
C-D-4-5

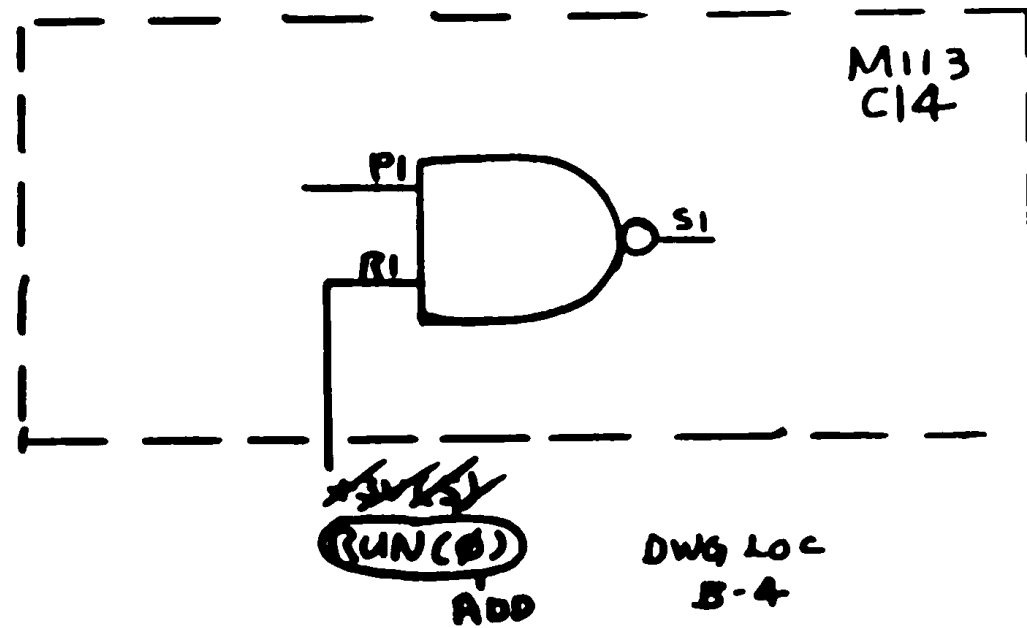
ECO 8L-00078 J-B5-8L-0-7 NEW REV. B



ECO 8L-00078 J-M4-8L-0-18 NEW REV. H

PDP8L-FCO-54

SHEET 3 1 3



DWG LOC
B-4

ECO 8L-00083 D-BS-8L-0-2 NEW REV L

PDP8L-FC0-57

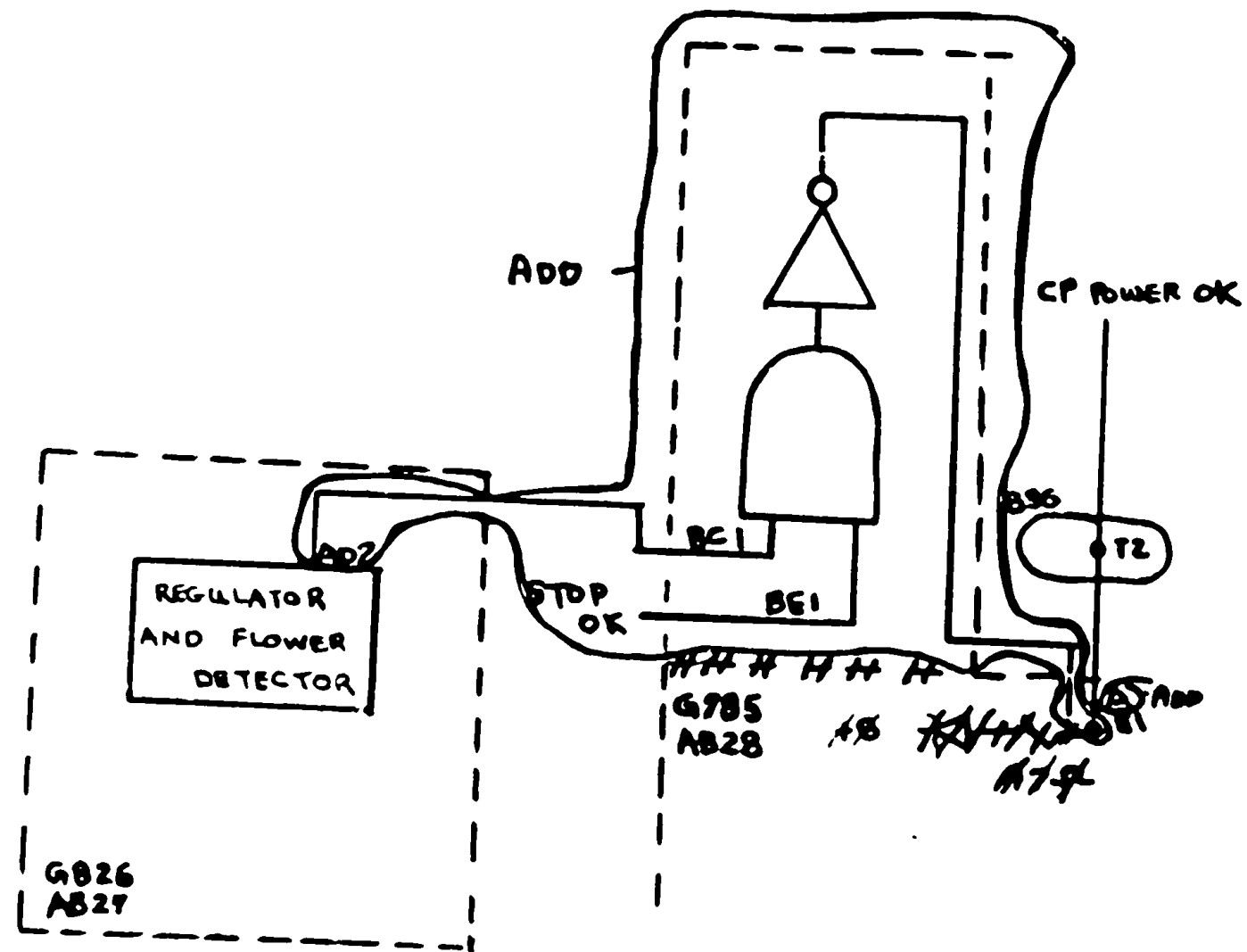
ADD/DELETE SHEET
MAKE ALL DELETIONS FIRST

SHEET 2 OF 4
PAGE 1 OF 1

COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	A27D2	A27D2	B28C1		X	
	STOP OK	D09K1	A36P2			X
		D09K1	B28E1		X	
	↓	B28E1	A36P2		X	

ECO NUMBER <u>8L 00085</u> MADE BY <u>J.W. PATE</u>		DRAWING NUMBER AFFECTED			
		CODE	SIZE	NUMBER	NEW REV LTR
		WL	K-8L 0-19		W

PNP8L-PCD-59



DWG LOC
B-6+7

ADD/DELETE SHEET
MAKE ALL DELETIONS FIRST

SHEET 2 OF 6
PAGE 1 OF 2

COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	D02S1	C04U2	D02S1			Y
	INT OK	B13U2	C15L2			X
		C15L2	D12J2			Y
		B13U2	C28E1		X	
		C28E1	D12J2		X	
WIRING CHANGES	INT STROBE	C04U2	C28H1		X	
		C28H1	D02S1		X	
	LOAD SF	B36M1	C15N2			X
		B36M1	C28J1		X	
	TS 4 (U)	C13F1	C15M2			X
		C15M2	D11L1			X
		C13F1	C28F1		X	
		C28F1	D11L1		X	
WIRE LIST CORRECTIONS ONLY (POINTS AND MACHINES ARE CORRECT)						

ECO NUMBER <u>8L-00088</u> MADE BY <u><i>ms</i></u>	DRAWING NUMBER AFFECTED			
	CODE	SIZE	NUMBER	NEW REV LTR
	WL	K-8L-0-19		Y

PDP8L-FCO-63

ADD/DELETE SHEET

SHEET 3 OF 6

MAKE ALL DELETIONS FIRST

PAGE 2 OF 2

COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	KEY COUNT	C02R2	C05P2			X
		C05P2	D02H1			X
		C02R2	D02H1		X	
	KEY COUNT	D01P2	C05P2		X	

DRAWING NUMBER AFFECTED

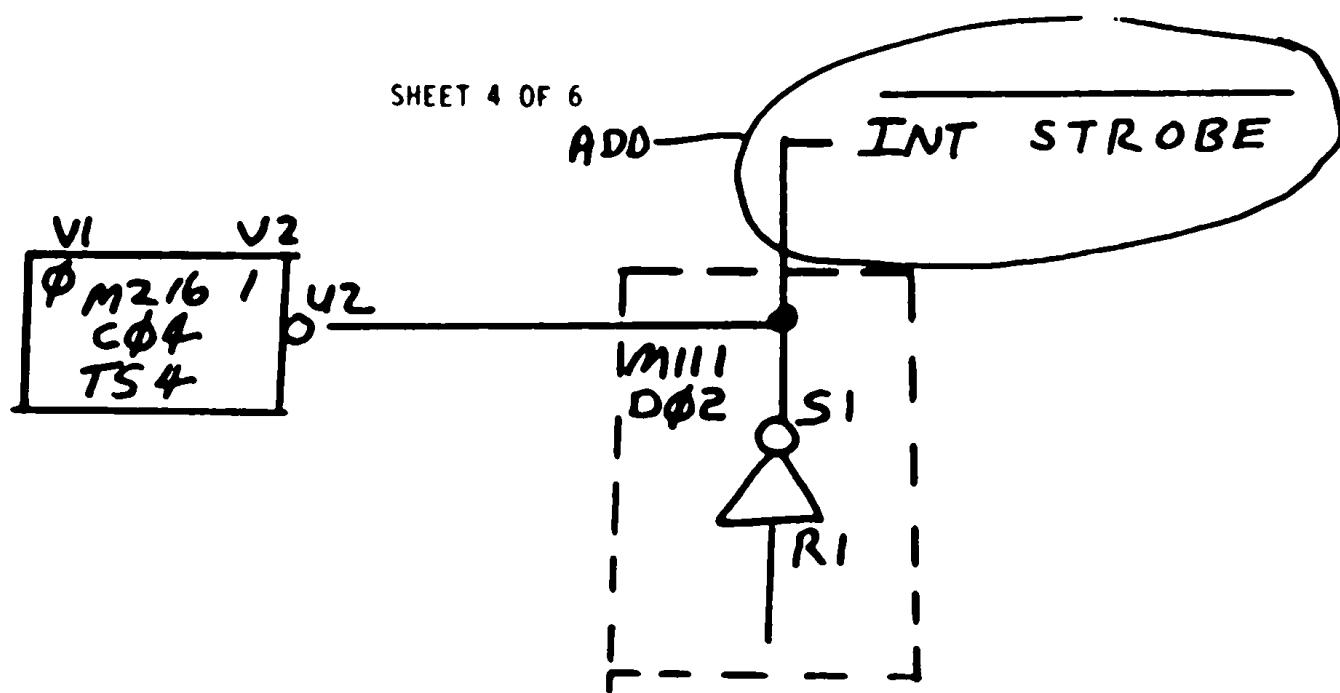
ECO NUMBER 8L 00088

MADE BY *Ans*

CODE	SIZE	NUMBER	NEW REV LTR
WL	Y-8L	0-10	Y

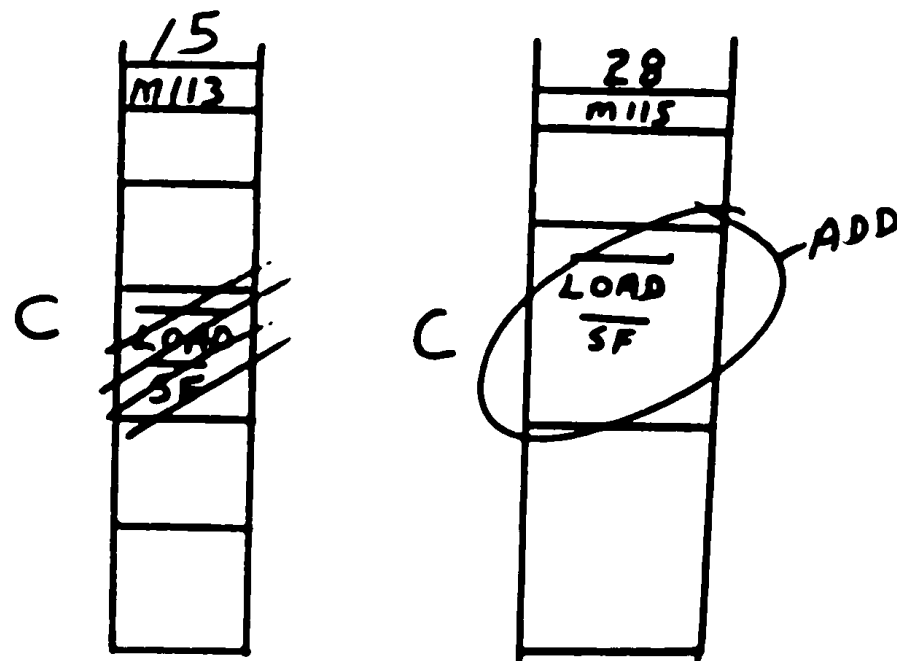
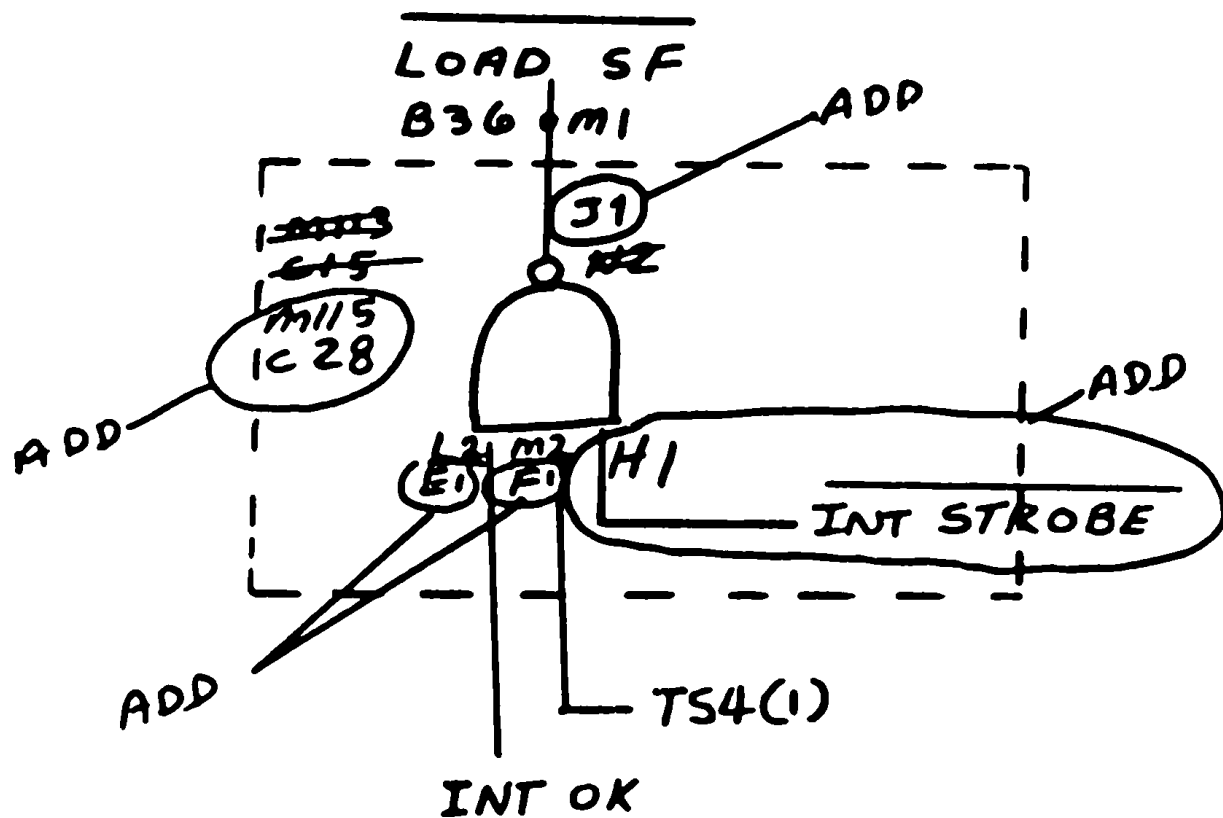
PDP8L-FCO-64

SHEET 4 OF 6



ECO # 8L-00088 D-BS-8L-0-2 NEW REV. M

PDP8L-FCO-65



ECO #BL-00088 D-BS-BL-0-17 NEW REV.C

ECO #BL-00088 D-MU-BL-0-18 NEW REV.J

PDP8L-PCD-66

ADD/DELETE SHEET
MAKE ALL DELETIONS FIRST

SHEET 2 OF 2
PAGE 1 OF 1

COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	GND 36	B36C2	B36T1			X
		B36R2	B36L2		X	
		B36L2	B36J2		X	
		B36J2	B36F2		X	
		B36F2	B36C2		X	
		B36R2	B36T1		X	
		D36C2	D36C1			X
		D36R2	D36L2		X	
		D36L2	D36J2		X	
		D36J2	D36F2		X	
	✓	D36F2	D36C2		X	
	GND 36	D36R2	D36C1		X	

ECO NUMBER <u>8L-00089</u> MADE BY <u>Ans</u>	DRAWING NUMBER AFFECTED		
	CODE	SIZE	NUMBER
	WL	K-8L-0-19	

NEW REV
LTR
Z

PDP8L-FCO-6A

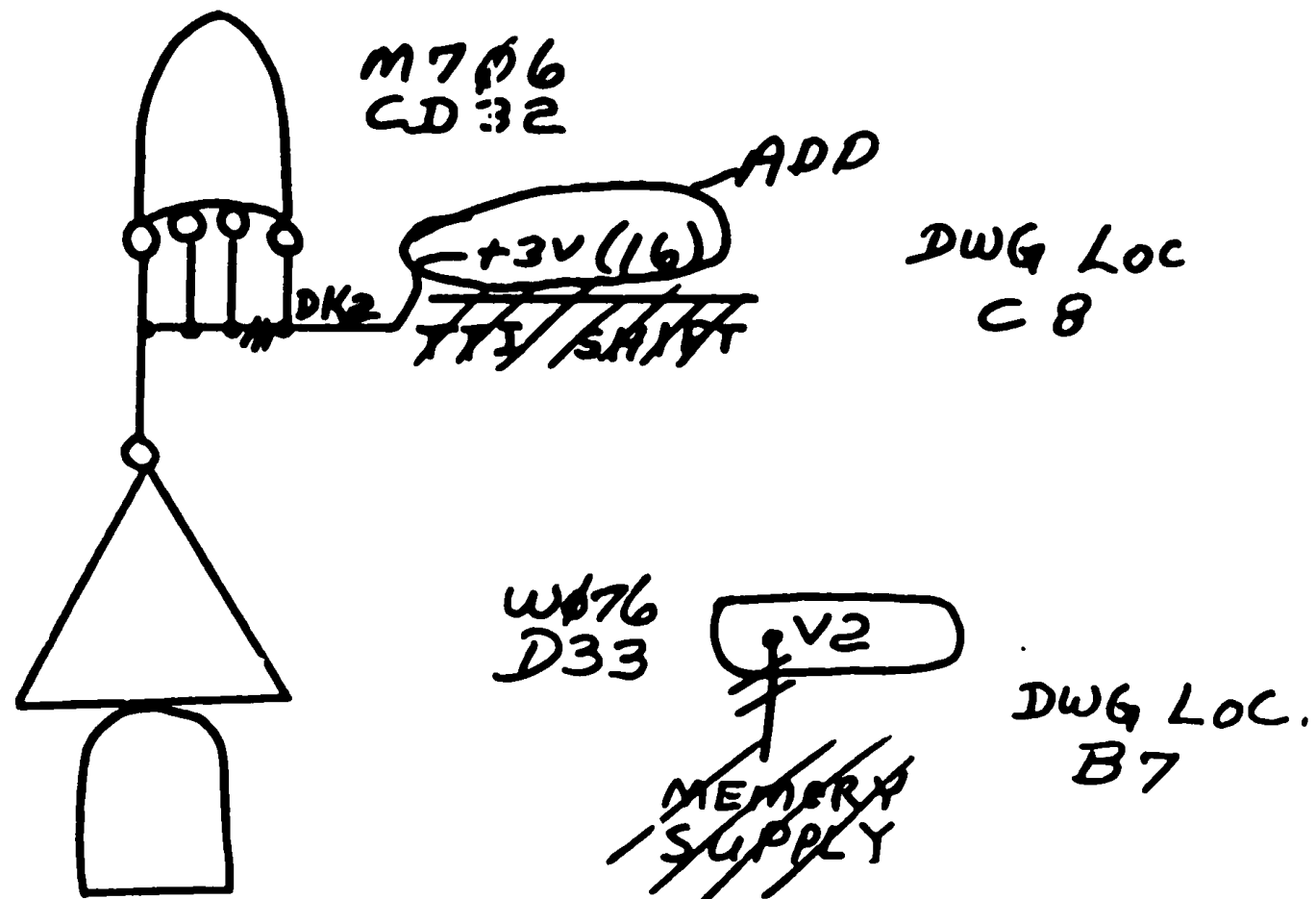
ADD/DELETE SHEET
MAKE ALL DELETIONS FIRST

SHEET 2 OF 3
PAGE 1 OF 1

COLOR	SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
	+3V (16)	D32R1	D32K2		X	
	MEM SUPPLY -	B23V2	B24V2			X
		B23V2	D33V2			X
	↓	B23V2	B24V2		X	

ECO NUMBER <u>8L 00000</u>		DRAWING NUMBER AFFECTED		
MADE BY <u>ONCS</u>	CODE	SIZE	NUMBER	NEW REV LTR
	WL	K-8L-0-13		AA

PDP8L-FCO-70



ECO # 8L-00090 D-BS-8L-0-11 NEW REV. B

SHEET 4 OF 4

SLOT D-36

ADDED

M903 *	
1 CABLE	2

* NOTE ADDED

ECO #BL-00091 DMU-BL-0-26 ^{NEW} REV K

PDP8L-FCO-75

digital EQUIPMENT CORPORATION

PARTS LIST ENGINEERING CHANGE ORDER

CHANGE NO. 8L-00106					
DOCUMENTATION PROJECT NUMBER					
ACT	PROD.LINE	TYPE OR DISCRETE			
	810	0	1	5	46
MANUFACTURING PROJECT NUMBER					
ACT	PROD.LINE	TYPE OR DISCRETE			

STOCK DISPOSITIONS CODE	ACTION TAKEN
SC=SCRAP RN=RETAIN	PARTS ADDED
NR=NEWWORK NC=NO CHANGE	NPO=NO PARTS ON ORDER
	POR=PARTS ON ORDER
	IS=PARTS IN STOCK

PRODUCTION ENGINEER	HIRSCH
DESIGN ENGINEER	M. ARSENAULT

PARTS DELETED

ITEM NO.	PART NO.	NO. DEL. PER UNIT	STOCK DISP.	DESCRIPTION
1	G717	1		RESISTOR TERMINATOR

PARTS ADDED

ITEM NO	PART NO	NO REQD PER UNIT	DESCRIPTION	ACTION TAKEN
1	M902-YA	1	RESISTOR TERMINATOR	
1	M902-YB	1	RESISTOR TERMINATOR	

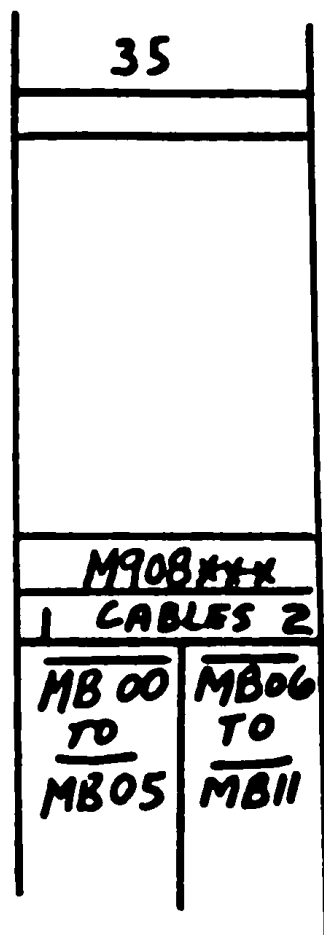
PDP8L-FCO-79

		QUANTITY VARIATION	
DWG NO./PART NO.	DESCRIPTION	PDP-8L	
GTM M902 YA	RESISTOR TERMINATOR	1	
M902 YB	RESISTOR TERMINATOR	1	

ADD SHEET 2 OF 2

ECO #8L-00106 A-PL-8L-0-18 NEW REV L

PDP8L-FCO-80



THE ~~G311~~ ^{ADD} M902-YB (TERMINATOR) IS ALWAYS PLACED AT THE END
 OF THE IO BUS FOR IOP'S, AC BITS AND ~~ADD~~ TIME STATES WHEN
^{ADD} 20 FEET OR MORE OF CABLE IS USED. WHEN THERE ARE NO
 OPTIONS THE ~~G311~~ ^{ADD} M902-YB IS PLACED IN LOCATION D36.

THE M902-YA IS ALWAYS PLACED AT THE END OF THE IO BUS FOR ALL MB
 BITS WHEN 20 FEET OR MORE OF CABLE IS USED. WHEN THERE ARE NO
 OPTIONS THE M902-YA IS PLACED IN LOCATION D35.



ENGINEERING CHANGE ORDER

ECO NO.
8L 00107
Sheet 1 of 2

ORIGINATOR ROBERT NUNLEY	RECEIVED CHG. DATE 12/14/70	ISSUED ECO DATE 12/28/70	FINAL RELEASE DATE 1/7/71	DISCRETE PROJECT NUMBER 60-05028
DATE 12/7/70	EXT. 2136			

EQUIPMENT AFFECTED

TYPE CHANGE	UNIT TO BE CHANGED	CHECKLIST	YES	NO
ELECTRICAL <input checked="" type="checkbox"/>	PDP 8L	SHOP MODEL	<input type="checkbox"/>	<input checked="" type="checkbox"/>
MECHANICAL <input type="checkbox"/>	PRODUCT LINES	SYSTEMS PROGRAMS	<input type="checkbox"/>	<input checked="" type="checkbox"/>
MODULE <input type="checkbox"/>		DIAGNOSTICS	<input type="checkbox"/>	<input checked="" type="checkbox"/>
SUBASSEMBLY <input type="checkbox"/>	PDP 9L	TECHNICAL PUB.	<input type="checkbox"/>	<input checked="" type="checkbox"/>
MFG/FIELD PROCEDURE <input type="checkbox"/>		TEST PROGRAMS	<input type="checkbox"/>	<input checked="" type="checkbox"/>
TEST INFORMATION		TESTER	<input type="checkbox"/>	<input checked="" type="checkbox"/>
SERIAL #		MFG/FIELD PROCEDURE	<input type="checkbox"/>	<input checked="" type="checkbox"/>
BY		PACKAGING INSTRUCTIONS	<input type="checkbox"/>	<input checked="" type="checkbox"/>
		TOOLING	<input type="checkbox"/>	<input checked="" type="checkbox"/>

BREAK IN POINT					SIGNIFICANCE	
SYSTEM	MODULE/OPTION	FIELD	RETROFIT	REWORK	CODE	
	PDP-8L					
						REQ TO MEET SPECIFICATION <input type="checkbox"/>
						PROD IMPROVEMENT <input type="checkbox"/>
						CUSTOMER/FIELD REQ. <input type="checkbox"/>
						FACILITATE MFG. <input type="checkbox"/>
						DRAWING CORRECTION <input checked="" type="checkbox"/>
						VENDOR <input type="checkbox"/>

PROBLEM ECO #8L 00062 DELETED PIN C04A1 FROM SIGNAL STROBE. THIS PIN IS SHOWN IN TWO PLACES ON DRAWING, AND SIGNAL NAME WAS ONLY CHANGE IN 1 PLACE.

FIELD SERVICE CODES
F

CORRECTION REMOVE PIN C04A1 FROM STROBE AND ADD IT TO A35H1. PRINT CORRECTIONS ONLY.

APPROVAL

DESIGN ENGINEER	MEL ARSENAULT	ENG MGR	REMO VOGELSONG
PRODUCTION ENGINEER	ALAN HIRSCH	FIELD SERVICE (ADVISORY)	ROBERT NUNLEY
CHIEF ENGINEER			

PDP8L-FCO-82

digital

ENGINEERING CHANGE ORDER
DOCUMENT & MATERIAL
CHANGE

ECO NO.

RL-00107

SHEET 2 OF 2

	DOCUMENT/OR PART NUMBER	OLD REV	NEW REV	(PART NAME) DESCRIPTION OF CHANGE	DISP
1	A-ML 8L-0	BE	BF	UPDATE REV FOR ITEM 2 OF THIS ECO	06
2	D BS-8L-0 2	M	N	(TIMING, MANUAL FUNCTIONS AND RUN) DELETE PIN C04A1 FROM SIGNAL STROBE ZONE D-7	06

DISPOSITION CODES

USE UP PRESENT STOCK	01	RETROFIT TO BREAK-IN	05
USE PRESENT STOCK UNTIL NEW STOCK AVAILABLE	02	DOCUMENTATION CHANGE ONLY	06
REWORK ALL MATERIAL	03	NEW ITEM PURCHASE	07
REWORK UNTIL NEW STOCK AVAILABLE	04	NEW ITEM IN STOCK	09
		NEW ITEM ON ORDER	09
		RETAIN	00

PDP8L-FCO-83



ENGINEERING CHANGE ORDER

ECO NO.
8L-00112
Sheet 1 of 3

ORIGINATOR LARRY NARHI	RECEIVED CHG. DATE	ISSUED ECO DATE	FINAL RELEASE DATE	DISCRETE PROJECT NUMBER
DATE EXT. 5/27/71 3629	6/11/71	6/16/71	6/29/71	81 07160

EQUIPMENT AFFECTED

TYPE CHANGE	UNIT TO BE CHANGED	CHECKLIST	YES	NO
ELECTRICAL <input checked="" type="checkbox"/>	PDP-8L	SHOP MODEL	<input type="checkbox"/>	<input checked="" type="checkbox"/>
MECHANICAL <input type="checkbox"/>	PRODUCT LINES	SYSTEMS PROGRAMS	<input type="checkbox"/>	<input checked="" type="checkbox"/>
MODULE <input type="checkbox"/>		DIAGNOSTICS	<input type="checkbox"/>	<input checked="" type="checkbox"/>
SUBASSEMBLY <input type="checkbox"/>	PDP-8L	TECHNICAL PUB.	<input type="checkbox"/>	<input checked="" type="checkbox"/>
MFG/FIELD PROCEDURE <input type="checkbox"/>		TEST PROGRAMS	<input type="checkbox"/>	<input checked="" type="checkbox"/>
TEST INFORMATION		TESTER	<input type="checkbox"/>	<input checked="" type="checkbox"/>
	SERIAL #	MFG/FIELD PROCEDURE	<input type="checkbox"/>	<input checked="" type="checkbox"/>
	BY	PACKAGING INSTRUCTIONS	<input type="checkbox"/>	<input checked="" type="checkbox"/>
		TOOLING	<input type="checkbox"/>	<input checked="" type="checkbox"/>

BREAK IN POINT				SIGNIFICANCE	
SYSTEM	MODULE/OPTION	FIELD	REWORK	CODE	
	PDP-8L	RETROFIT <u>ONLY</u> AT REQUEST AND EXPENSE CUSTOMER.		01	REQ TO MEET SPECIFICATION <input type="checkbox"/>
					PROD IMPROVEMENT <input type="checkbox"/>
					CUSTOMER/FIELD REQ. <input checked="" type="checkbox"/>
					FACILITATE MFG. <input type="checkbox"/>
					DRAWING CORRECTION <input type="checkbox"/>
					VENDOR <input type="checkbox"/>

PROBLEM WHEN CLEARING TTY KEYBOARD FLAG, READER RUN IS SET, CAUSING TYPE TO ADVANCE THIS IS UNDESIRABLE IN SOME PROGRAMMING SITUATIONS.

CORRECTION CLEAR FLAG WITH IOP4 (READ BUFFER) AND SET READER RUN WITH IOP2. THIS ECO TO BE INSTALLED ONLY AT CUSTOMER REQUEST.

APPROVAL
 DESIGN ENGINEER M. ARSENAULT 5/27/71 ENG MGR R. VOGELSANG
 PRODUCTION ENGINEER T. MACDONALD FIELD SERVICE (ADVISORY) R. NUNLEY
 CHIEF ENGINEER _____

PDP8L-ECO-84

digital

ENGINEERING CHANGE ORDER
DOCUMENT & MATERIAL
CHANGE

ECO NO.

8L-00112

SHEET 2 OF 3

	DOCUMENT/OR PART NUMBER	OLD REV	NEW REV	(PART NAME) DESCRIPTION OF CHANGE	DISP CODE
1	D-BS-8L-0-11	B	C	ADD FOLLOWING NOTE: TO ALLOW TOP4 TO CLEAR KEYBOARD FLAG: DELETE D32D1 TO GND DELETE C32E2 TO C32V2 ADD D32D1 TO C32V1 ADD C32V2 TO D32E2	06
2	A-ML 8L-0	BK	BL	UPDATE REVS PER THIS ECO	06

DISPOSITION CODES

USE UP PRESENT STOCK	01	RETROFIT TO BREAK-IN	05
USE PRESENT STOCK UNTIL		DOCUMENTATION CHANGE ONLY	06
NEW STOCK AVAILABLE	02	NEW ITEM PURCHASE	07
REWORK ALL MATERIAL	03	NEW ITEM IN STOCK	09
REWORK UNTIL NEW STOCK		NEW ITEM ON ORDER	09
AVAILABLE	04	RETAIN	00

PDP8L-FCO-85

digital

ENGINEERING CHANGE ORDER
ADD/DELETE SHEET

ECO NO.
RL 00112
SHEET 3 OF 3

WIRE LIST NO. D 85-8L Ø 11

TITLE

MAKE ALL DELETIONS FIRST WHEN INSTALLING

SIGNAL NAME	FROM PIN	TO PIN	COMPONENTS	ADD	DEL
KCC	C32V2	C32E2			x
GND	D32D1	D32C2			x
GND	D32D1	D32T1			x
TTAC CLEAR	D32E2	C32V2		x	
KRB	D32D1	C32V1		x	
GND	D32T1	D32C2		x	

PDP8L-FCO-86



ENGINEERING CHANGE ORDER

ECO NO.
-L 00113
Sheet 1 of 3

ORIGINATOR ED REED	RECEIVED CHG. DATE 6/18/71	ISSUED ECO DATE	FINAL RELEASE DATE 7/22/71	DISCRETE PROJECT NUMBER 99 06224
DATE 6/15/71	EXT. 2174			

EQUIPMENT AFFECTED

TYPE CHANGE ELECTRICAL <input checked="" type="checkbox"/> MECHANICAL <input type="checkbox"/> MODULE <input type="checkbox"/> SUBASSEMBLY <input type="checkbox"/> MFG/FIELD PROCEDURE <input type="checkbox"/>	UNIT TO BE CHANGED PDP 8L PRODUCT LINES TPL	CHECKLIST SHOP MODEL <input type="checkbox"/> YES <input checked="" type="checkbox"/> NO SYSTEMS PROGRAMS <input type="checkbox"/> YES <input checked="" type="checkbox"/> NO DIAGNOSTICS <input type="checkbox"/> YES <input checked="" type="checkbox"/> NO TECHNICAL PUB. <input type="checkbox"/> YES <input checked="" type="checkbox"/> NO TEST PROGRAMS <input type="checkbox"/> YES <input checked="" type="checkbox"/> NO TESTER <input type="checkbox"/> YES <input checked="" type="checkbox"/> NO MFG/FIELD PROCEDURE <input type="checkbox"/> YES <input checked="" type="checkbox"/> NO PACKAGING INSTRUCTIONS <input type="checkbox"/> YES <input checked="" type="checkbox"/> NO TOOLING <input type="checkbox"/> YES <input checked="" type="checkbox"/> NO
TEST INFORMATION SERIAL # 2818 BY ED REED		

BREAK IN POINT				SIGNIFICANCE	
SYSTEM	MODULE/OPTION	FIELD	RETROFIT	REWORK	CODE
	8L	ONLY WHEN A BM8-L MEMORY EXPANSION IS INSTALLED.		Y	
					REQ TO MEET SPECIFICATION <input type="checkbox"/> PROD IMPROVEMENT <input type="checkbox"/> CUSTOMER/FIELD REQ. <input checked="" type="checkbox"/> FACILITATE MFG. <input type="checkbox"/> DRAWING CORRECTION <input type="checkbox"/> VENDOR <input type="checkbox"/>

PROBLEM
EXTENDED ADDRESS BITS "0" AND "1" ARE OPEN COLLECTOR AND HAVE NO PULLUP RESISTORS WHEN A BM8 L IS INSTALLED.

CORRECTION
WIRE IN CLAMP LOADS WHICH ARE AVAILABLE IN THE PDP-8L.

APPROVAL
DESIGN ENGINEER _____ J. MILTON _____ ENG MGR _____
PRODUCTION ENGINEER _____ R. REED _____ FIELD SERVICE (ADVISORY) _____
CHIEF ENGINEER _____ R. NUNLEY _____

PDP8-FCO-87

digitalENGINEERING CHANGE ORDER
DOCUMENT & MATERIAL
CHANGE

ECO NO.

- 00113

SHEET 2 OF 3

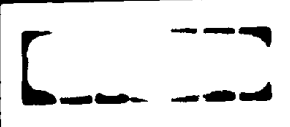
	DOCUMENT/OR PART NUMBER	OLD REV	NEW REV	(PART NAME) DESCRIPTION OF CHANGE	DISP CODE
1	A-ML-8L-0	BL	BM	UPDATE PER ITEMS THROUGH 5	06
2	K-WL-8L-019	AD	AE	SEE MARKED PRINT	06
3	D-BS-8L-0 10	C	D	MAKE FOLLOWING ADDITIONS TO LOCATION B-2 ON THE PRINT: CLAMP -LOAD- M906, B33T2 TO CABLE-M903, C35T2; AND CLAMP LOAD-M906, B33V2 TO CABLE- M903, C35V2.	06
4	D-MU-8L 0-18	M	N	UPDATE PER ITEM 3	06
5	A-PL-8L-0 18	M	N	UPDATE REV PER ITEM 4.	06

DISPOSITION CODES

USE UP PRESENT STOCK	01	RETROFIT TO BREAK-IN	05
USE PRESENT STOCK UNTIL NEW STOCK AVAILABLE	02	DOCUMENTATION CHANGE ONLY	06
REWORK ALL MATERIAL	03	NEW ITEM PURCHASE	07
REWORK UNTIL NEW STOCK AVAILABLE	04	NEW ITEM IN STOCK	09
		NEW ITEM ON ORDER	09
		RETAIN	00

PDP8L-FCO-88

TECH.
TIPS.

	FIELD SERVICE TECHNICAL MANUAL				Option or Designator
	12 Bit <input checked="" type="checkbox"/>	16 Bit <input type="checkbox"/>	18 Bit <input type="checkbox"/>	36 Bit <input type="checkbox"/>	PDP-8L

Title <u>BUFFERING OF POWER CLEAR</u>				Tech Tip PDP-8L TT#1 Number	
Processor Applicability		Author Robert Nunley	Rev #	Cross Reference	
AN		Approval W. Cummins	Date 7-31-72		

The Power Clear signal run, generated at A25S2 is overloaded beyond engineering specs. However, because we use the level rather than transition, this overload is acceptable in most machines. In the rest, due to component age and component individual characteristics, weird unexplainable things might happen with any or all of the following symptoms.

1. Intermittent halt when none was programmed (not to be confused with loss of timing where run is on but there is no control of the machine) where run is cleared as if the halt key as actuated.
2. Intermittent loss of data where one memory cell is changed to 0000.
3. Intermittent clearing of flags and/or buffers in I/O devices (not connected to a DM01).

If any of these symptoms occur it is possible that the cause is the power clear run.

If a glitch appears on power clear this is what can happen:

1. If the glitch appears before TP3 but after TP2 memory control flops will be cleared and as a result one memory location will be cleared, but the MB will have the correct data this time. TP3 will then set RUN and the program should resume normal flow (until the zero's are reached again).
2. If the glitch appears after TP3 the effect is as if the SS key is pressed.
3. Depending on where the glitch occurs between MEM start and strobe governs whether or not a read is done at all, or a strobe is generated.
4. If the glitch appears in the 8L of amplitude and duration enough to cause any of the above, it will be felt on the I/O bus and cause the same type intermittent problems.

To buffer Power Clear: break the Power Clear run at A27S2 but maintain the other end (could go to D16A1 or B13R1 depending on the vintage of the 8L).

Add A27S2 to C27E2
Add C27J2 to other end of wire deleted in the first step.
Add 220 ohm 1/4W pull up

C27J2 to +5V

This gives a drive of about 100 load units for the Power Clear run.

PAGE 1	PAGE REVISION 0	PUBLICATION DATE July 1972
--------	-----------------	----------------------------

COMPANY CONFIDENTIAL

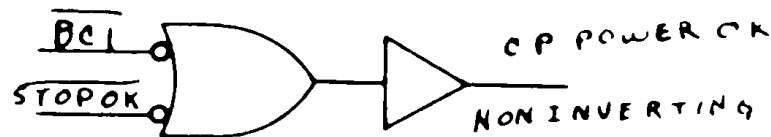
PDP8L-TT-1

Title PRINT CORRECTIONS IN 8L				Tech Tip Number PDP-8L TT #2	
Processor Applicability		Author Robert Nunley		Rev 0	
AN	8L	Approval W. Cummins		Date 7-31-72	
				Cross Reference	

There are errors in the 8L print set not in Logic Gating but in signal names and generation. Two of these errors have been corrected by ECO's which will be coded "P" therefore will not be distributed to the field.

The corrections are:

1. Drawing No. D-BS-8L-~~1~~-2 coordinates D-7 direct clear of TS1 is not ~~stroke~~, but the "OR" function of ~~POWER CLEAR + STROBE~~. The signal comes from Inverter M11 at A35H1. (This gating was generated by ECO 8L-00045, ECO 8L-00059, ECO 8L-00062.) Direct Clear of TS1 should now be called "A35H1."
2. Drawing number D-BS-8L-~~1~~-13 coordinates B-6 generation of "CP Power OK." The logic works correctly but should be drawn like this.



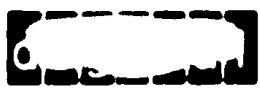
Title 8L ECO 00045, 00056 ERROR				Tech Tip Number PDP8/L TT#3	
Processor Applicability		Author Art Newbury		Rev 0	
AN	8L	Approval W.E. Cummins		Date	
				Cross Reference	

Another ECO will be generated to effect correction of an error which exists with respect to ECO's 8L #00045 and 00056. The schematic which is part of the Speco for 8L 00056, shows correctly that there are three inputs to the M15 which is added in slot C28. The Add/Delete sheet, however, fails to include the wiring of the TS4 (B) input to C28B1. The following Add will resolve the problem:

C28B1 to C64V1

COMPANY CONFIDENTIAL

PDP8L-TT-2

	FIELD SERVICE TECHNICAL MANUAL				Option or Designator
	12 Bit <input checked="" type="checkbox"/>	16 Bit <input type="checkbox"/>	18 Bit <input type="checkbox"/>	36 Bit <input type="checkbox"/>	PDP-8L

Title 3 CYCLE BREAK INTERMITTENT				Tech Tip Number PDP8L-TT-4	
All	Processor Applicability			Author S. Lamotte	Rev 0
	8L			Approval W.E.Cummins	Date Aug 15
					Cross Reference

PROBLEM: 3 Cycle break devices, with cables over 15ft. in length, have displayed a problem of intermittently not setting "Break". This is caused by "Ext 3 cycle L" being noisy at the processor. This condition brings up WC Set, when it shouldn't be there.

FIX: Ground "ext 3 cycle L" signal at processor, A34V2 B34C2

This Tech Tip apply's only to systems with 3 cycle break options, and no 1 cycle break devices.

Title 8L FUSE RATINGS				Tech Tip Number PDP8L-TT#5	
All	Processor Applicability			Author W. Freeman	Rev 0
	8L			Approval W.Cummins	Date 12/1/73
					Cross Reference

Because of the complaints about the 5 amp fuse in the +5 volt line (F-3) in the 718 power supply, an investigation was undertaken. The following facts have been revealed:

- 1) The PDP-8L draws six amps on the +5 volt line under full load conditions.
- 2) This load will occasionally blow the 5 amp fuse (F-3)
- 3) The other two fuse ratings were found to be correct.

It was found that the use of a 6 amp fuse for F-3 produced reliable operation. An ECO for changing this specification (#718-00007) is being prepared for distribution. This will be a permanent change for all 718 power supplies. The proper fuse ratings for the 718 power supply are:

F-1	15 amp slow blow
F-2	4 amp slow blow
F-3	6 amp common

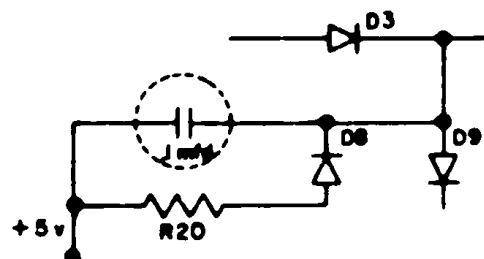
COMPANY CONFIDENTIAL

PDP8L-TT-3

Title					Tech Tip				
8L AFFECTED BY EXTERNAL NOISE					Number PDP8L-TT-7				
Processor Applicability					Author W. Freeman				
All					Rev g				
8L					Approval W. E. Cummins				
					Date 11/14/73				
					Cross Reference				

TECH TIP NUMBER PDP8L-TT-6
WAS NEVER PUBLISHED.

If external noise from surrounding equipment is affecting an 8L, the installation of a .1 mfd. capacitor on the G785 module as shown in the schematic below will resolve the problem.



Mel Arsenault

March 1969

Approximately 150 8/L's have been shipped to the field with the capacitor connected to the base of Q1 rather than the junction of D3, D8, and D9. A symptom indicative of this condition is random stopping in the run state, all switch functions inoperative and inability to restart except following a power-down, power-up operation.

Title					Tech Tip				
B RUN NOISE					Number PDP8L-TT-8				
Processor Applicability					Author W. Freeman				
All					Rev g				
8L					Approval W. Cummins				
					Date 12/05/73				
					Cross Reference				

B RUN NOISE

It has been found that ground noise caused by the MB bits is causing B RUN to move. The noise can be eliminated by tying the emitters of all the transistors on the M623 together.

CO's M623 - 00001 and 00002 implement this solution.

Title					Tech Tip				
DOUBLE SELECTION OF G221					Number PDP8L-TT -9				
Processor Applicability					Author W. Freeman				
All					Rev g				
8I 8L					Approval W. E. Cummins				
					Date 12/05/73				
					Cross Reference				
					G221-TT-1				

COMPANY CONFIDENTIAL

PDP8L-TT-4

Title CONVERSION FROM TTY TO LA36, .20MA CURRENT LOOP			Tech Tip Number PDP8L TT 10	
Author R. LANE/H. KESSELMAN		FS Office MAYNARD	Date 3.14.78	Revision 0
Processor Applicability		Mgr /Sup R. ALVAREZ	Date 3.14.78	Cross Reference
A		Approval	Date	LA36 TT 19

THIS TECH TIP IS FOR CROSS REFERENCE ONLY.